

Rochester Institute of Technology

RIT Digital Institutional Repository

Theses

8-2014

Process Cooperativity as a Feedback Metric in Concurrent Message-Passing Languages

Alexander Dean

Follow this and additional works at: <https://repository.rit.edu/theses>

Recommended Citation

Dean, Alexander, "Process Cooperativity as a Feedback Metric in Concurrent Message-Passing Languages" (2014). Thesis. Rochester Institute of Technology. Accessed from

This Thesis is brought to you for free and open access by the RIT Libraries. For more information, please contact repository@rit.edu.

**Process Cooperativity as a Feedback Metric
in Concurrent Message-Passing Languages**

APPROVED BY

SUPERVISING COMMITTEE:

Dr. Matthew Fluet, Supervisor

Dr. James Heliotis, Reader

Dr. Rajendra K. Raj, Observer

**Process Cooperativity as a Feedback Metric
in Concurrent Message-Passing Languages**

by

Alexander Dean, B.S.

THESIS

Presented to the Faculty of the Golisano College of Computer and Information Sciences

Department of Computer Science

Rochester Institute of Technology

in Partial Fulfillment

of the Requirements

for the Degree of

Master of Science

Rochester Institute of Technology

August 2014

Acknowledgments

I wish to thank the multitudes of people who helped me, but time would fail me to tell of all the ways they aided me throughout my time at RIT and to the completion of this document. However, I owe my deepest gratitude to my supervising committee for all of their support and guidance in this process. The corrections and contributions of all readers, in particular Professor James Heliotis and Michael Swan, have made for a much better document, one which I can be proud of. My chair, Professor Matthew Fluet, was the one who showed me the road which lead to this degree, without him I would not have been able to follow my heart. Last, but certainly not least, my family and my wife Kana Kennedy, have been the driving force and source of encouragement in my life. I love you all sincerely and owe all success I achieve to you.

Abstract

Process Cooperativity as a Feedback Metric in Concurrent Message-Passing Languages

Alexander Dean, M.S.

Rochester Institute of Technology, 2014

Supervisor: Dr. Matthew Fluet

Runtime systems for concurrent languages have begun to utilize feedback mechanisms to influence their scheduling behavior as the application proceeds. These feedback mechanisms rely on metrics by which to grade any alterations made to the schedule of the multi-threaded application. As the application's phase shifts, the feedback mechanism is tasked with modifying the scheduler to reduce its overhead and increase the application's efficiency.

Cooperativity is a novel possible metric by which to grade a system. In biochemistry the term cooperativity is defined as the increase or decrease in the rate of interaction between a reactant and a protein as the reactant concentration increases. This definition translates well as an information theoretic definition as: the increase or decrease in the rate of interaction between a process and a communication method as the number of processes increase.

This work proposes several feedback mechanisms and scheduling algorithms which take advantage of cooperative behavior. It further compares these algorithms to other common mechanisms via a custom extensible runtime system developed to support swappable

scheduling mechanisms. A minimalistic language with interesting characteristics, which lend themselves to easier statistical metric accumulation and simulated application implementation, is also introduced.

Table of Contents

Acknowledgments	iii
Abstract	iv
List of Tables	viii
List of Figures	ix
Chapter 1. Introduction	1
Chapter 2. Background	4
2.1 Message-Passing	4
2.2 Classic Runtime Scheduling	7
2.3 A Note on Control Theory	10
2.4 Feedback-Enabled Scheduling	12
2.4.1 Cooperativity as a Metric	15
Chapter 3. Methodology	17
3.1 Overview	17
3.2 ErLam	17
3.2.1 The ErLam Language	18
3.2.2 Channel Implementations	19
3.2.3 The Scheduler API	22
3.2.4 Example Usage: The CML Scheduler	24
3.2.5 Provided Schedulers	25
3.3 Simulation & Visualization	27
3.3.1 Runtime Log Reports	28
3.3.2 Cooperativity Testing	30
3.4 Cooperativity Mechanics	32

3.4.1	Longevity-Based Batching	32
3.4.2	Channel Pinning	35
3.4.3	Bipartite Graph Aided Sorting	37
Chapter 4.	Results and Discussion	40
4.1	Evaluation	40
4.1.1	Test Case Implementation	41
4.1.2	Scheduler API	43
4.1.3	Channel Implementations	49
4.2	Cooperativity Mechanics	51
4.2.1	Longevity-Based Batching	52
4.2.1.1	Boundary-Case Scenarios	53
4.2.1.2	Time-Quantum Effects	56
4.2.2	Channel Pinning	58
4.2.2.1	Random and Uniform Communication	59
4.2.3	Bipartite-Graph Aided Sorting	60
4.2.3.1	Random and Uniform Communication	61
4.2.3.2	Optimal-Case Scenario	64
4.3	A Comment on Swap Channels	65
Chapter 5.	Conclusion and Future Work	68
5.1	Utility of the ErLam Toolkit	68
5.2	Effectiveness of Cooperativity as a Metric	68
5.3	Future Work	68
Appendix		70
Appendix 1.	ErLam Test Cases	71

List of Tables

4.1	Comparison of $STRR$ and $STDQ$ using Channel State, Reduction and Communication Densities.	45
4.2	Comparison of $MTRRWS-IS$ and $MTRRWS-SQ$ using Scheduler State and Queue Size.	48
4.3	Comparison of two runs of $Interactivity_{(16,8)}$ on $MTRRWS-SQ$ using either Absorption or Blocking swap channels.	50
4.4	Comparison of $ChugMachine_N$ spread on the Longevity-Batching Scheduler and $MTRRWS-SQ$	54
4.5	Re-run of $ChugMachine_N$ with Longevity-Batching Scheduler in ‘singleton’ batching mode.	55
4.6	Comparison of different sized $PRing_N$ on the Longevity Batching Scheduler with batch size $B = 10$	56
4.7	Comparison of $PTree_{(4,8)}$ running with the Longevity-Based Batching Scheduler and $MTRRWS-SQ$ at different time-quantums.	57
4.8	Comparison of Random and Uniform synchronization for $MTRRWS-SQ$ and the Channel Pinning Scheduler on Absorption Channels.	60
4.9	Channel State comparisons of Random and Uniform synchronization for $MTRRWS-SQ$ and the Bipartite-Graph Aided Sorting Scheduler using Absorption Channels.	62
4.10	Channel State comparisons of Random and Uniform synchronization for $MTRRWS-SQ$ and the Bipartite-Graph Aided Sorting Scheduler on Blocking Channels.	63
4.11	Channel State comparison of Parallel Fibonacci executed on $MTRRWS-SQ$ and the Bipartite-Graph Aided Sorting Scheduler.	64

List of Figures

2.1	A High-Level Message-Passing Taxonomy	4
2.2	A classical feedback loop representation [11].	11
2.3	Two subcomponents formed by process cooperativity. Black dots represent processes, and the white dots represent a channel.	15
3.1	The ErLam language grammar, without syntax sugar or types.	18
3.2	Syntactic sugar parse transformations.	19
3.3	A simple ErLam application which swaps on a channel before returning. . .	20
3.4	Channel operation over time. Note arbitrary time-slice t_1 is when the first swap operation is evaluated.	21
3.5	The ErLam Scheduler API	22
3.6	CML Process Spawning.	24
3.7	CML Process evaluation.	25
3.8	Example of Communication Density graph for the Work-Stealing scheduler on a Core i7 running the <i>PRing</i> (defined in section 3.3.2) application. . . .	29
3.9	Simulated behavior examples and test primitives.	33
3.10	Bipartite Graph of Processes (P s) to Channels (C s).	38
4.1	Our implementation for a map-reduce style fork-branch, and its subsequent standardized usage.	41
4.2	Parallel Fibonacci implementation and a potential channel graph.	42
4.3	The tick-disparity for over nearly 900 tests.	44
4.4	A naive but ineffectual <i>ClusterComm</i> _(N,M) implementation.	66
1.1	ChugMachine application, generates N processes to just compute.	71
1.2	UserInput application, utilized in the Interactivity composure, it artificially hangs without anything to do for a random period of time so as to simulate an interactive process.	72
1.3	ClusterComm application, generates N processes and M channels, and waits for at least $N - M$ processes to finish synchronizing X times. . . .	73

1.4	PRing application, simulates full-system cooperativity, where N processes pass a token around a ring network.	74
1.5	PTree application, utilizes composure of ClusterComm to simulate partial-system cooperativity as a set of work-groups.	75
1.6	Interactivity Application, utilizes a composure of UserInput.els and Chug-Machine.els to test a scheduler's ability to handle interactivity.	76
1.7	JumpShip application, similar to PTree and ClusterComm, except posses to demonstrate application phases as the work-groups change channels.	77

Chapter 1

Introduction

Runtime systems can be broken up into several distinct parts: the garbage collector, dynamic type-checker, resource allocator, *etc.* One subsystem of a language's runtime is the *task scheduler*. The scheduler is responsible for ordering task evaluation and the distribution of these tasks across the available processing units.

Tasks¹ are typically spawned when there is a chance for parallelism, either explicitly through `spawn` or `fork` commands or implicitly through calls to parallel built-in functions like `pmap`. In either case it is assumed that the role of a task is to perform some action concurrent to the parent task because it would be quicker if given the chance to be parallel. It is up to the scheduler of these tasks to try and optimize for where there is opportunity for parallelism. However, it's not as simple as evenly distributing the tasks over the set of processing units. Sometimes, these tasks need particular resources which other tasks are currently using, or perhaps some tasks are waiting for user input and don't have anything to do. Still worse, some tasks may be trying to work together to complete an objective, and rely on dynamic dependencies that change over time.

Message passing is a common alternative to, and sometimes abstraction of, shared memory. Some implementations of message passing are akin to emailing a colleague a question. You operate asynchronously, and your colleague can check her mailbox and then respond at her leisure. Meanwhile you are free to wait until she gets back to you, or ask

¹We will be using the terms *task*, *job*, and *process* interchangeably throughout this document.

someone else in the mean time. Other implementations require you to wait on your colleague's reply before continuing.

While message passing is a good method for inter-process communication, it is also a nice mechanism for catching when two processes are working together. For example, consider a purely functional `pmap`, where all workers are given subsections of the list. Each worker thread will have no need to access another's subsection and thus no messages will need to be passed. However, what happens when the function being mapped on a particular subsection uses several processes? Each may access a shared resource via message passing. We would observe a close coupling of processes in this case. This highlights the granularity of process coupling, in that the `pmap` workers exhibit coarse-grained parallelism, which allows the scheduler greater flexibility to run them in parallel. The opposite is true for the processes which show close coupling, like the mapped function. We define this granularity of process coupling as *Process Cooperativity*.

There are many mechanisms that scheduling systems can use to improve workload across all processing units. Some of these mechanisms use what is called a *feedback system*. Namely, they observe the running behavior of the application as a whole, (*i.e.* collect *metrics*), and modify themselves to improve operation.

Process Cooperativity is an interesting metric by which to grade a system. In biochemistry the term cooperativity is defined as an increase or decrease in the rate of interaction between a reactant and a protein as the reactant concentration increases. We can translate this into an information theoretic definition:

Definition 1. *The degree of cooperativity of a **process** is the increase or decrease in its rate of interaction with an inter-process communication method, and subsequently, the degree of cooperativity of the **system** is the rate of interaction between the sets of processes and channels as the concentration of both fluctuate.*

Thus, when a process attempts to pass a message to another we know it's trying to cooperate on some level. When this frequency of interaction is high, it may indicate a tight coupling of processes or fine-grained parallelism. If it is low, this could indicate coarse-grained parallelism. In either event, a scheduler able to recognize these clusters of cooperative and non-cooperative processes should have an edge over those that don't.

Chapter 2 will look first at the background of classical scheduling systems as well as the recent feedback-enabled approaches. Then, we will also examine the types of message passing implementations and how these effect scheduling decisions, now that we are looking at process cooperativity. Chapter 3 introduces our work on a language and compiler, built to easily simulate system cooperativity and visualize the effects of scheduling mechanisms on these systems. We also discuss a few example mechanics which take advantage of cooperativity. Some example applications which demonstrate different degrees of cooperativity and phase changes are also explained. In Chapter 4 we run our cooperativity-enabled schedulers along with a few common non-feedback-enabled schedulers on the example applications and discuss the results. Finally, in Chapter 5 we give some concluding remarks and avenues of future research we believe would be fruitful.

Chapter 2

Background

2.1 Message-Passing

In concurrent systems, there are a number of methods for inter-process communication. Arguably though, one of the more popular abstractions is the idea of message passing. This is especially true in functional languages as the language assumes shared-nothing by default. Also, just as compilers can optimize using language constraints, so can a runtime using the language implementation. We will therefore examine possible message passing designs and how their implementation might effect our schedulers.

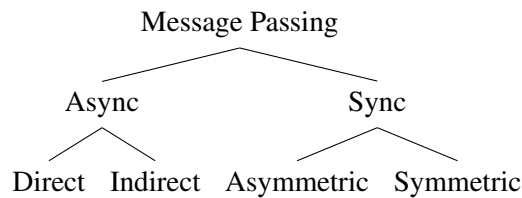


Figure 2.1: A High-Level Message-Passing Taxonomy

Message passing in general can be broken down into two types based on the language's implementation; asynchronous or synchronous. Asynchronous message passing is like our example of emailing a coworker. The two actors in the scenario can perform their communication operations at different times and do not rely on the other's completion. Granted, a receive will not complete successfully until after a send has finished. Synchronous message passing however, requires that both actors block on their respective operations until the message pass has completed.

In *direct* asynchronous message passing, a process can send the message directly to another process like in our example of emailing a coworker; these implementations are aptly named mailbox message passing. An alternative implementation may send the message *indirectly* via a rendezvous point, like a socket.

To send a message in either case requires pushing/copying the message into a shared or global memory space for another process to access (possibly) at a later time. This push/copy can be done in a lock free manner with some lower level atomic data structures such as a double-ended queue. But in either a locked or lock-free manner, the process performing the send still forces a block somewhere along in the operation, to push the message into shared storage. While asynchronous operations free user-code to continue, its implementation now requires the additional capabilities to both store and resend a message at a later time. So at some point, a process will need to synchronize with a block of memory to enforce exclusive access. If the implementation were to ignore this, contention could occur between two internal processes attempting communication operations with the same block; either overwriting or corrupting what was already there.

In terms of scheduling, a language with asynchronous message passing will not hint much in regard to whether progress is being made. If a consumer process requires a value before continuing and therefore is repeatedly trying to receive from the channel, the schedule for the system would be better served by coming back to that process at a later time rather than repeatedly looping. However, asynchronous can benefit from process placement so as to take advantage of possible gains in cache affinity [1].

For example, the effects of the cache on direct message passing (*e.g.* a process mailbox) can be substantial if two processes on different cores share a location to store and therefore check for content. This shared location, if accessed from two cores will need to be updated in possibly multiple locations and validated for consistency at the cache level. However, if the two processes are local to the same cache there will be time saved in context

switching and cache-line validation. In indirect message passing the task can be even worse as it's common that more than two processes may need access to the same space. Note, however, that recognizing cache effects can also lead to gains in synchronous message passing.

In synchronous message passing, a process must meet another at a provided rendezvous point but can either be symmetrical or asymmetrical. Note that the rendezvous point is not a requirement in the sense that direct synchronous messaging isn't possible. Instead we think of a rendezvous point in synchronous communication to be time bound rather than location bound (*i.e.* two processes are blocked until communication occurs, the implementation of this passing is irrelevant to this classification).

Asymmetrical message passing is synonymous with Milner's Calculus of Communicating Systems [2] or standard Π -Calculus [3], in that you have a sender, and then a receiver which will both block on their respective functions around an anonymous channel until the pass has been completed. This differs from symmetrical message passing, where the only operation on the channel is a blocking function which swaps values with the process at the other end.

It's worth noting that asynchronous message-passing can be simulated using synchronous channels with a secondary buffer process. But by simulating it in this fashion we, as the scheduler, elevate the problem of cache locality to a problem of process locality. The same methods suggested to alleviate some of the lost efficiency due to cache locality [4, 5] are the same techniques which could be simulated for process locality; namely process batching and process affinity.

Note also, it is possible to simulate symmetrical message passing on asymmetrical message channels, but in terms of scheduling of synchronizing processes, order is now a factor that needs to be considered. On top of this, directionality can also be a factor

which complicates the channel implementation. Namely, the internal queuing of senders or receivers may not percolate hints up to the scheduler regarding their queue position.

For the alternative, symmetrical message passing or swap channels, the order is directly handled by the scheduling of the system (*i.e.* the order at which the scheduler evaluate the *swap* command can be directly governed). And it is for this purpose along with simplifying our core language we have chosen to base our semantics on symmetric synchronous message-passing.

2.2 Classic Runtime Scheduling

Operating Systems research has long been the leading front for scheduling topics. However, most of the early concern in scheduling was devoted to job scheduling over a group/shared system. As such, their concerns were largely devoted to fairness and job priority. They frequently had *a priori* knowledge regarding their jobs which gave them an opportunity to decide on a complete schedule beforehand.

Instead, the systems for which we can schedule must be defined differently. A runtime scheduler may only have access to the processes which it observes, and can only speculate about their future based on their current state and any recorded historical information.

A scheduler's operations can be defined in a discrete set of time-steps where, through our definition, must perform three operations for each step. First, it takes the set of processes and chooses one. This selection operation can be as simple as popping from the top of a queue but is defined by the private state of the scheduler. As such, it can change with time or be based on the recorded historical state of the processes.

Next, it will perform some reduction action on the chosen process; taking into account the private-scheduler and global-channel state. This reduction could result in a

modification of the scheduler’s private-state (*i.e.* due to additional historical records), the global channel state (*i.e.* with the creation of a new message passing channel), or possibly the generation of another process, in the case of a process spawn.

Finally, the scheduler will update the process set with the reduced process (and any generated processes). This update may also modify the private state of the scheduler, which could store statistics like a process’s historical information. This state update can influence its selection for the next step, like choosing the oldest process, or the one last seen, *etc.*

We base our Scheduler API (section 3.2.3) on this idea of a constant tick function, which maps the state of the scheduler’s world to the world after one time-step. Multiple schedulers can then be run in parallel using the same step semantics, except now the total process queue must be shared or partitioned depending on the scheduler’s unique implementation.

However, outside of this unique formalism, there is a large set of classical methodologies for these operations (*e.g.* selection, reduction, process set update). We now turn to a description of some of these to lay a basis for our later design discussion.

One popular selection operation is the First-Come, First-Serve method, which means ordering the processes in a queue and running them continuously as they spawn and enqueueing them only as they block. However, if a particular process is computationally intensive, processes involved with user-interaction for example would need to wait. This results in an obvious lag or hang in the system as the interactivity of the system stalls to finish computation.

To solve this problem a scheduler can *preempt* a process after a certain amount of time has passed. This time slice is also called a time interval or a *quantum* and has quite a literature involved with its selection [6, 7, 8]. Too short, doesn’t allow a process enough time to progress and the runtime system starts to spend more time context switching than

computation. Too long and the preemptive-scheduler effectively becomes non-preemptive as all computation-bound processes hog the CPU from the interactive ones.

We translate this idea of a quantum to the simple step formalism by allowing the scheduler to keep track of a reduction count. This reduction count influences where the update operation places the process at the end of the step. If it has reached some max reduction count, then it could be enqueued at the end of the process set. Alternatively it can be placed at the beginning to be selected again until after preemption.

This strategy, to enqueue at the end upon preemption, is known as *round-robin scheduling*. It is a common choice not necessarily due to its simplicity, but its fairness. Each process in the queue is guaranteed an equal amount of time on the CPU and starvation of processes can therefore never happen. However, this isn't always the case as it depends on how process spawning is implemented. For example, if the newly spawned process was placed at the front of the queue or preempts the currently running process, a fork-bomb like process could hog the CPU and effectively shut out all other processes. Spawning to the end of the queue is the only effective way to avoid these scenarios with round-robin scheduling.

This, however, has all been using the assumption of a single process queue. While it is possible to implement a single global queue for all P processors, we will eventually get into an issue of contention where all the processors are attempting to take or add a process to the queue while another one is. However, in the event of multiple process queues there needs to be a mechanism in place for dispersing the processes across them all in an even or fair way.

There are two mechanisms for this, namely, *work-sharing* and *work-stealing*. In work-sharing, the processor with more than enough work to do, will delegate any new processes to another (either randomly or by some heuristic). In work-stealing, it's the scheduler with the empty or small process queue that contacts another scheduler (either randomly or by some heuristic) so as to steal one or more queued processes. In the case of work-stealing

the victim processor can be working on a process while another processor steals from it. In turn, the cost of performing the process transfer is potentially masked by the parallelism gained. In the case of work-sharing there is always an additional overhead incurred on top of process execution as the overloaded processor must wait until the delegated process is transferred.

This is why most schedulers which support multiple processing units utilize some work-stealing implementation. Of the implementations, there are two which we would like to highlight as they are provided by the ErLam toolkit: Shared-Queues and Interrupting-Steal. The Shared-Queues work-stealing scheduler allows other processes to directly access an end of their local process queue. This means, while a processor is potentially popping from one end of the queue, another could be stealing from the other end (assuming a lock-free doubly-ended queue like structure).

The alternative, Interrupting-Steal, has gone by several names like Work-Requesting, and Thief Processes. Its mechanism is to send a fake or dummy process to one or more other schedulers so when they run them they steal a process and send it back to its parent process. This reduces the overhead involved in synchronizing on the victim's process queue, but will instead stall it during the steal.

2.3 A Note on Control Theory

There has never been a scheduler which works optimally in all cases. Since for any scheduler, a program can be feasibly designed for which the scheduler produces a sub-optimal schedule. Instead, focus has been the most fruitful when pursuing the optimization of various measurements using some particular objective function [9] to tune for particular edge cases. As such, scheduling based on such feedback metrics is not a new practice [10].

There is a big distinction though, which can be made between the effects of control theory in classical cybernetic applications versus that of runtime systems. This is primarily

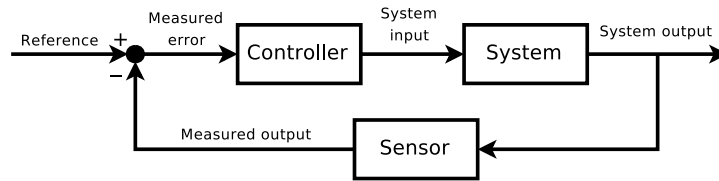


Figure 2.2: A classical feedback loop representation [11].

in the adaptation of the controller in the generic feedback loop (figure 2.2).

The classic feedback loop starts by reading the current state of the system and applying some operation to it (via the controller). The operation in some way affects the system which can be observed by measuring particular metrics. These measurements can be fed back to the controller along with details about the system's output. If the controller wishes to modify the system further via the same or an opposite operation, it may do so. The canonical example is that of an automobile's cruise control. The controller can correct the speed of the vehicle by applying or releasing the throttle based on readings of the current speed so as to maintain a desired speed.

In typical physical feedback loops there are two scenarios which need to be avoided: resonance and rapid compensation. Resonance in physical systems is when a spike in the amplitude of a system's oscillation can cause it to fail at a particular frequency. It can be seen that most controller models will attempt to damp the adjustments to reduce oscillation which could cause resonance or sharp spikes in behavior based on its output. This is due to the limitations of the physical space in which they are having to work. But frequent or extreme damping or can stress physical systems to the point of failure as well.

However, in runtime scheduling systems we would very much like to do the opposite. We would prefer tight oscillations or consistent behavior of our runtime so as to achieve minimal overhead from our modifications. We can also compensate, to reach our reference signal, as quickly as we need to as there are no physical restrictions for our modifications.

As such these feedback systems are closely coupled with the design of the scheduling algorithm, rather than being an interchangeable sensor, and controller modules. As such we make an effort to trace the feedback optimizations during our evaluation and explanation of the scheduler designs.

2.4 Feedback-Enabled Scheduling

Operating systems have also had motivation for designing intelligent feedback-enabled schedulers. As systems move away from perfect knowledge about the jobs it will be running, scheduling has needed to make guesses about the length of time jobs will need to run. A well-known example to this effect is called the Multi-Level Feedback Queue (MLFQ) scheduler, first described by Corbató *et al.* [12, 13].

The scheduler maintains N separate process queues, for N priority levels. All new processes would be spawned to the highest priority and would be subsequently demoted if they ended up running their whole designated quantum. However, a process may inadvertently game the system by running just up to the quantum before yielding. To fix this, after some time, S , the MLFQ is reset and all processes are boosted to the highest priority. This helps with adapting to new system behavior which may arise as well as coping with process starvation.

The goal of the MLFQ model is two-fold: to prefer interactive processes and to subsequently reduce the strain of computation bound processes on the overall system. This allows the system to prune the short-running processes out quickly and also maintain an adequate level of interactivity. A MLFQ implementer would also be able to heuristically set the quantum, N , and S based on the needs of the system as it's running, so as to introduce a second layer of feedback. For example, one could observe how much of a particular time period each priority queue is using. If a lower priority queue is being starved, it could trigger a reset [14].

The MLFQ idea in general is highly malleable and can be adapted to a number of situations. As such it transferred well into the level of runtime systems quite well. Concurrent ML (CML), uses this idea of a MLFQ to improve application interactivity.

CML is an extension to SML which adds the *spawn* function, and channel operations, among other things (such as synchronous events) [15]. CML's scheduler defines a MLFQ where $N = 2$ and uses a single promotion algorithm instead of a reset. However, there is a key difference: CML uses process tagging to mark whether a process has communicated in its last time quantum.

As all newly spawned processes are appended to the primary queue, CML tells the difference between these newcomers and the short-running processes by tagging any process which makes a communication, or demoting it if not. A promotion can only happen if a previously marked process gets a demotion. However, the demotion process of a marked process is just a mark removal. Thus, the primary queue is essentially two queues in one.

CML's dual-queue system has the effect of reacting to new processes by testing them for longevity. It then makes an assumption about their behavior immediately, but a process can change the scheduler's first impression of them through consistent behavior to the contrary. A marked communication-bound process will, if it continues to use its entire quantum, eventually be demoted. A computation-bound process can eventually be promoted and marked as a communication-bound if it continues to communicate. Thus the system eventually adapts its behavior to the new phase of the process.

However, recently an alternative mechanism has been utilized to adapt to system behavior, that of process batching. The *occam- π* language, and specifically the Kent Retargetable *occam* Compiler (KR_oC), allows processes which frequently communicate to be batched and processed together [16]. This has two side effects: cache-affinity, and informed work-stealing.

The goal of the KRoC scheduler is primarily to take advantage of cache-locality when scheduling processes. It does so by reducing the chances for cache-misses by grouping processes which have a higher likelihood to communicate. The concept being, if two processes communicate, the data which is being shared will be in cache unless too many context-switches forces it out, thus place them close together in the queue. As a side effect of this, instead of stealing single processes, the KRoC schedulers will steal batches from each-other. This results in a quicker equilibrium in work-load saturation than stealing single processes.

Process migration between batches is done in two ways: 1. A channel synchronizes and causes the process to be de-scheduled from one scheduler and sent to the one which unblocks it. 2. A batch is split when more than one process in a batch is active, by popping the head of the batch into a new one. We explain this de-scheduling method in greater detail in Section 3.2.2, as we've implemented this mechanism for testing purposes. However, the mechanism absorbs a blocking process into the channel it's blocked on until another process unblocks it. At that time, the scheduler which unblocked it, now becomes its owner. Occam- π uses this mechanism as a method to build up batches of cooperating processes.

Ritson *et al.* mention however, that without a method to break up the batches, the system will eventually become one large batch. Therefore, whenever a new process joins a batch, the batch is allowed to split if there are more than one currently active processes within it (*e.g.* non-blocked or waiting processes). Thus, if a parent spawns a large number of processes (*i.e.* passed the batch size limit), the parent can start a new batch, while the batch of children can be stolen.

While KRoC's primary goal was cache-affinity, and CML's was optimizing interactivity, their feedback systems enabled a more efficient schedule than would have otherwise been possible with a classical scheduler focused entirely on work-saturation. We now discuss another feedback metric, *Process Cooperativity*, which KRoC's scheduler, and our

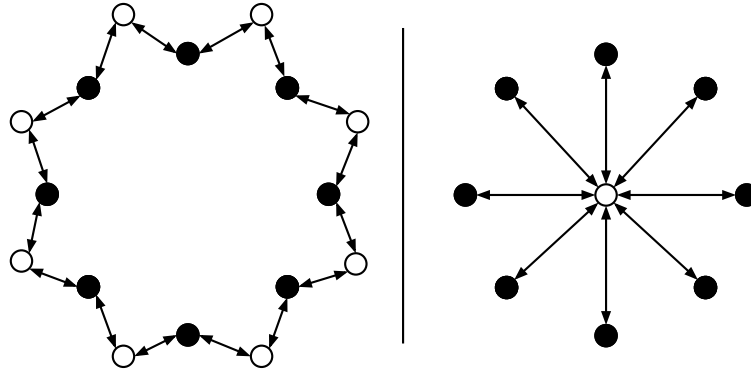


Figure 2.3: Two subcomponents formed by process cooperativity. Black dots represent processes, and the white dots represent a channel.

algorithms presented later, were able to benefit from.

2.4.1 Cooperativity as a Metric

Process Cooperativity stands out as a critical feedback metric in process-oriented programming. In fact the KRoC scheduler showed this through their performance gains. They showed that when a scheduler can recognize when two or more processes form a subcomponent, treating them that way improves cache utilization, reduces context switching time, and makes for smarter work-stealing. From this, we can take that recognizing cooperativity gives a good mechanism for determining a potential for fine-grained parallelism.

However, we would like to revisit the concern Ritson *et al.* expressed regarding when a component becomes too large. They introduced the mechanism of splitting batches based on an arbitrary max size of a batch, without regard to the substructure of the component expressed by the processes cooperation.

To illustrate this problem, figure 2.3 visualizes two possible components which may occur naturally. On the left we get a ring like structure, where the dependency of one process is the one to its right. We can abstractly envision a data-flow like application which acts like

a token ring network. On the right we have a cluster of processes, all communicating with a random other on the same channel. We can envision this as an abstraction over a single shared resource.

Both the ring and cluster subcomponents would gradually become grouped into a KRoC batch. This is optimal for the ring component, no matter how large the ring may be. There is nothing to be gained from splitting it into multiple batches, and by doing so, we may actually hinder it. However, the cluster component may improve if given the chance for more parallelism, this would depend entirely on the *longevity* of the processes (*i.e.* the extent the process is computation-bound). KRoC attempts to account for this by recognizing if there are multiple active processes in a batch, and splitting an arbitrary one into a new batch (which ever happens to be at the head of the process queue).

While this may not be avoidable based on observing structure alone, we may now run into an issue. Suppose all processes are active, but run for a length of time under their designated time quantum. At every preemption, when the size of the batch forces a split, we will create a singleton batch which must be reabsorbed after a single run. Ignoring the overhead, fairness properties also start to percolate. Namely, the processes within the batch after being trimmed will get preferential treatment to the processes in the singleton batches. This is exacerbated in the case of a single processor, as all singleton batches would need to wait for the large batch to run. Inevitably, the worst-case scenario for KRoC is below that of the work-stealing scheduler.

From this we can take that the longevity of a process can affect its cooperativity. In fact, we can look back at definition 1, and its application to a single process. A process' *degree of cooperativity* can be defined as its *frequency* of interaction with a set of channels. Thus, a cooperativity-conscious scheduler should also want to consider both the longevity of a process, and which channels it communicates with. This would give a much more complete picture of cooperativity.

Chapter 3

Methodology

3.1 Overview

To examine the effects of cooperativity-conscious schedulers we needed to have a method for comparing several scheduler implementations without needing to modify the underlying implementation of processes, channels, or application source code. It would be also beneficial if our solution were able to visualize these differences similar to Haskell’s ThreadScope [17].

Our solution, *ErLam*, is a compiler for an experimental version of Lambda Calculus with Swap Channels and a runtime system which allows for swappable scheduler mechanisms and an optional logging system which can be fed into a custom report generator. We break up our solution description into three parts; Section 3.2 will discuss our language syntax and semantics. It will also demonstrate our Runtime Scheduler API by breaking down the CML Interactivity scheduler. Section 3.3 will go more into depth about our testing environment which involves our logging system, the report generator, and the set of example applications we used to represent different cooperativity levels. Finally, Section 3.4 will go over our example schedulers we wrote which demonstrate cooperative-conscious behavior. These will be the schedulers we provide our results against.

3.2 ErLam

The ErLam toolkit is itself broken down into three parts, the language and its semantics, the Runtime System, and the Scheduler API. We will first lay out the language and

```

<Expression> ::= <Variable>
               | <Integer>
               | 'newchan'
               | '(' <Expression> )'
               | <Expression> <Expression>
               | 'if' <Expression> <Expression> <Expression>
               | 'swap' <Expression> <Expression>
               | 'spawn' <Expression>
               | 'fun' <Variable> '.' <Expression>

```

Figure 3.1: The ErLam language grammar, without syntax sugar or types.

its basic semantics, as the finer-details are reliant on the exact selected scheduling solution as well as the chosen swap-channel implementation. We will then examine the possible channel implementations and how they affect the given semantics. Next, we will discuss the Scheduler API using an example scheduler implementation. We conclude this chapter with a summary of each of the classic schedulers that are included in the ErLam toolkit.

3.2.1 The ErLam Language

The ErLam Language is based on Lambda Calculus, with first-class single variable functions, but deviates somewhat in that it provides other first-class entities. It deviates from Church representation to provide Integers, however, this is purely for ease of use. It also provides a symmetric synchronous channel type for inter-process communication.

Figure 3.1 expresses ErLam in its simplified BN-Form. The semantics for the language is fairly straight forward. All expressions reduce to one of the terminal types: Integer, Channel, or Function. To spawn, for instance, if any terminal is passed other than a function, it returns a 0 (*e.g.* false). When a function is passed, it is applied with *nil* to initialize the internal expression in another ErLam process, and evaluates to 1 (*e.g.* true) in the parent process.

ErLam also makes a number of ease-of-use decisions like providing a default branch operator and a library system for providing a set of built in functions such as numeric oper-

$$\begin{aligned}\mathbf{let } x = e_1 \mathbf{ in } e_2 &\Rightarrow ((\mathbf{fun } x.e_2) e_1) \\ \mathbf{fun } x,y,z.e &\Rightarrow \mathbf{fun } x.(\mathbf{fun } y.(\mathbf{fun } z.e))\end{aligned}$$

Figure 3.2: Syntactic sugar parse transformations.

ations, type checking, and standard functional behaviors (*e.g.* combinators, *etc.*). However, these built-ins will be largely ignored in this document but explained when necessary. The branching operator works like C in that zero is interpreted as false and non-zero is true.

ErLam also extends this base grammar with some useful syntactic sugar (see figure 3.2 for syntactic transformations) such as SML style *let* expressions and multi-variable function definitions (which are curried from left to right). We will use the syntactic sugar throughout this document to make our source easier to review.

Also, note that the semantics of *swap* is to block until a successful swap. However, the implementation of the channel synchronization is free to change as long as it adheres. We take advantage of this to provide multiple channel implementations which will highlight distinct scheduling differences. We demonstrate this, along with an example ErLam application in the following section.

3.2.2 Channel Implementations

ErLam provides a selection of channel implementations to allow for interchangeable scheduler comparisons with different synchronization methods. We chose two channel implementations the *Blocking* Swap, and the *Absorbing* Swap as they highlight key differences for the runtime. We will now look at an example application and its execution using both methods for comparison.

Figure 3.3 gives an example ErLam application. It first creates a new channel for processes to communicate on. It then creates a null-function to spawn, whose sole purpose

```

let c = newchan in
let f = (fun _ . (swap c 42)) in
let _ = (spawn f)
in (swap c 0)

```

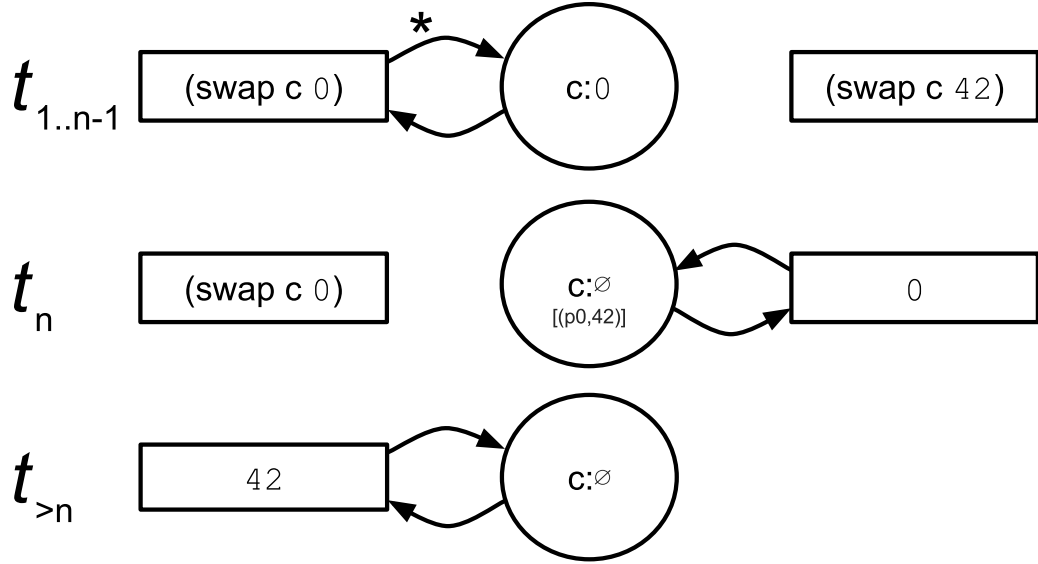
Figure 3.3: A simple ErLam application which swaps on a channel before returning.

is to swap on the channel the number 42 and quit. Finally, it swaps on the channel the number 0 and returns the result of the whole evaluation, which in this case will be the value passed from the other end of the swap, 42.

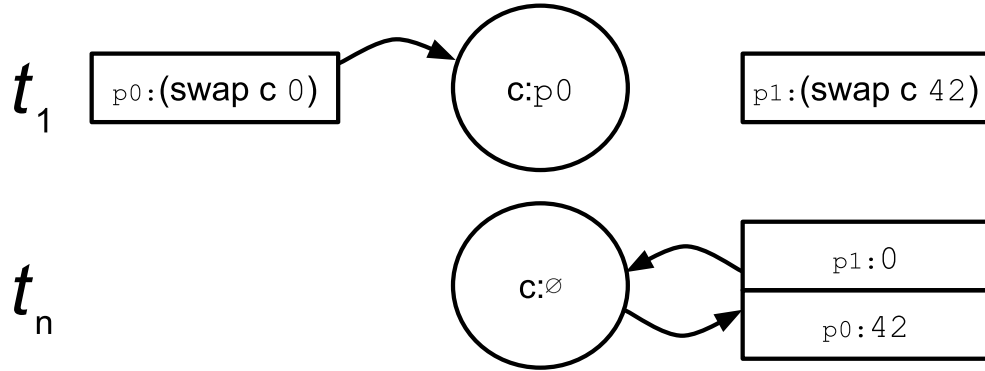
As ErLam is innately concurrent, we do not know which process will ask to swap first. It may even be possible that 0 asks to swap several times before 42 even tries. In fact, the *Blocking* channel allows this behavior of multiple swap attempts. We can see an illustration of this in figure 3.4(a). The first row shows the arbitrary time-slice t_1 where the process swapping 0, p_0 , first contacts the channel with its value. The process may be scheduled again, repeatedly, to check the channel up to some arbitrary time-slice t_{n-1} . At t_n however, the process swapping 42 requests a swap and immediately gets a value back and logs that the process which it swapped with can get its value when it returns. Thus on the third line, for any arbitrary time-slice in the future $t_{>n}$, the process p_0 can ask for a swap and get the value p_1 stored.

Note the illustration makes no explicit mention of the scheduler or its functionality. It may be the case that the two processes are on different processing units and are in different process queues. Or it may be the case that they both exist in the same queue and upon a block, the scheduler chooses the next one, which will immediately unblock the channel.

The *Blocking* channel effectively simulates a common spin-lock over a shared piece of memory. These channels represent a worst-case, albeit common, application implementation for concurrent software. Even so, they do allow for some hints to the scheduler, which can be taken advantage of. Alternatives on the spin-lock could be added to the Er-



(a) Process Blocking Swap



(b) Process Absorption Swap

Figure 3.4: Channel operation over time. Note arbitrary time-slice t_1 is when the first swap operation is evaluated.

```

-callback layout( erlang:cpu_topology(), scheduler_opts() ) ->
    scheduler_layout().

-callback init( scheduler_opts() ) ->
    {ok, scheduler_state()} |
    {error, log_msg()} |
    {warn, log_msg(), scheduler_state()}.

-callback cleanup( scheduler_state() ) ->
    ok | {error, log_msg()} | {warn, log_msg()}.

-callback tick( scheduler_status(), scheduler_state() ) ->
    {ok, scheduler_status(), scheduler_state()} |
    {return, term()} | {stop, scheduler_state()}.

-callback spawn_process( erlam_process(), scheduler_state() ) ->
    {ok, scheduler_state()} | {error, log_msg()}.

```

Figure 3.5: The ErLam Scheduler API

Lam toolkit though, such as a push-notifying semaphore, however we provide a simpler and functionally more common alternative: the process absorption channel.

In the *Absorption* channel (figure 3.4(b)), the first process to get to the channel will get absorbed by it. The scheduler which evaluated the swap will be out a process, and but the scheduler which unblocks the channel by arriving second will get back two processes (the one performing the swap, and the absorbed one). In terms of scheduling efficiency this type of message passing channel has provided enormous improvements for runtimes which do not wish to introduce channel inspection into their scheduler.

3.2.3 The Scheduler API

ErLam was written in Erlang, and as such, can take advantage of Erlang’s callback behavior specifications. An *erlam.scheduler* behavior was defined which requires a minimum of 5 callback functions (figure 3.5).

Upon instantiation the runtime system will call the *layout/2* function with the Non-

uniform Memory Access (NUMA) layout of the system that the application is running on, along with any parameters the user specified at runtime. The result of this function is to be the scheduler layout.

For example, let's assume we are running our application on a Intel Core i7 which has 4 logical cores which support hyper-threading. The *layout/2* function will be given the following structure:

```
[{processor, [{core, [{thread, {logical, 0}}, {thread, {logical, 1}}]},  
             {core, [{thread, {logical, 2}}, {thread, {logical, 3}}]},  
             {core, [{thread, {logical, 4}}, {thread, {logical, 5}}]},  
             {core, [{thread, {logical, 6}}, {thread, {logical, 7}}]}]}].
```

This indicates to the scheduler implementation that it, at max, can spawn 8 instances of itself which would be bound to each logical processing unit (LPU). Although we could of course have a scheduler which acts differently based on the architecture. However, the schedulers we have limited ourselves to are either single or fully multi-core (*i.e.* uses all available LPUs).

To spin up an instance of the scheduler on the particular core, the *init/1* function is called which should return the scheduler's state. As Erlang is a functional language, we use this state object as a means to maintain some global state for each scheduler process by threading it through all subsequent callback calls. Upon shutdown, the opposite function *cleanup/1* is called.

The last two functions are the most interesting as they pertain to the core of what each new scheduler provides, namely how to evaluate the world in a given time-slice (*tick/2*) and how a new process should be handled (*spawn_process/2*). An explanation of these callbacks is best done through example.

```

spawn_process( Process, State ) ->
    enqueueAndSwitchCurThread( Process, State ).

enqueueAndSwitchCurThread( Process, #state{curThread=T}=State ) ->
    case T of
        nil ->
            setCurThread( Process, State );
        _ ->
            % New process takes over
            {ok, NewState} = enqueue1( T, State ),
            setCurThread( Process, NewState )
    end.

```

Figure 3.6: CML Process Spawning.

3.2.4 Example Usage: The CML Scheduler

CML’s scheduler utilizes a dual-queue structure rather than a simple unary-process-queue. The scheduler attempts to differentiate between *communication* and *computation*-bound processes so as to reduce the effects of highly computationally intensive processes from choking the system. The scheduling system thus improves on application interactivity by demoting *computation*-bound processes to the secondary queue (which isn’t accessed until another process is demoted).

Spawning a process in the CML scheduler (figure 3.6) does not go onto the primary queue, instead we enqueue the current process and start evaluating the new process¹. This is a fairly simplistic example, but it shows how one would go about updating the state between ticks. Note also, that the *spawn_process/2* call happens on the same scheduler instance which evaluated the *spawn*. While this is not of consequence for this scheduler, a multi-core scheduler could be confident in appending a new process to its local queue without interfering with another LPU’s scheduler.

¹This scheduler therefore ignores the possibility of a fork-bomb like application in favor of a spawn mechanism which caters to average applications.

```

tick( _Status, #state{ curReduct=0 }=State ) ->
    {ok, NState} = pick_next( State ),
    reduce( NState );
tick( _Status, State ) -> reduce( State ).

pick_next( State ) ->
    {ok, NewState} = preempt( State ),           % Place cur thread onto queue
    {ok, Top, Next} = dequeue1( NewState ),      % Pop next off
    setCurThread( Top, Next ).                  % Set as cur and return state

```

Figure 3.7: CML Process evaluation.

In the original CML scheduler, it defined a quantum which it would let the current process run for, it would preempt it if it attempted to run for longer. The ErLam runtime avoids the use of time based quantum as logging and other factors directly affect the usefulness of this. Instead it uses a ‘tick’, which emulates one step forward in the execution of the application. Thus to simulate a quantum we instead keep track of the number of reductions performed on the current process and decrement the counter until we reach 0.

The *tick/2* function (figure 3.7) performs one of two things based on what the state of the system is. If the current reduction count is 0, then we can pick a new process from the queue, otherwise we can perform a reduction.

Note for our scheduler simulation we ignore the first parameter to the *tick/2* function for either case. The first parameter was the status of the scheduler returned from the previous tick (*e.g.* running, waiting, *etc.*). This would be useful if the CML scheduler utilized work-stealing to get work to do from other LPUs when in *waiting* mode.

3.2.5 Provided Schedulers

Along with the Single-Threaded Dual-Queue CML scheduler (*STDQ*), ErLam comes with several basic scheduling mechanics. We utilize these as bases cases on which to compare the behavior of all subsequent feedback-enabled schedulers.

- **The Multi-Threaded Round-Robin Global-Queue Scheduler (*MTRRGQ*)**

This scheduler uses a single global process queue which all schedulers share and attempt to work from. There is no rearrangement of order, and all threads will round-robin the queue (performing a set number of reductions per process before enqueueing and popping the top one).

- **The Single-Threaded Round-Robin Scheduler (*STRR*)**

This scheduler is a special case where $P = 1$. We provide a simplified implementation which removes all synchronization of the previous scheduler. This will be our simplest scheduler test case, and will be compared against *STDQ*.

- **The Multi-Threaded Round-Robin Work-Stealing Scheduler (*MTRRWS*)**

An improvement on the previous scheduler. Instead of a global process queue, each scheduler maintains their own. A waiting scheduler will randomly sleep-and-steal until it finds a process to work on from another scheduler. The provided implementation gives two example stealing mechanisms:

- **Shared-Queue (*MTRRWS-SQ*)**

Stealing a process involves performing an atomic dequeue from the bottom (rather than the top) of another scheduler's process queue. This will only block the other scheduler from performing a dequeue for a very short window of time, but the other scheduler must compensate for this concurrent access by synchronizing on its usage as well, despite the relative infrequency of steals.

- **Interrupting-Steal (*MTRRWS-IS*)**

Simulates sending a thief-process over to another scheduler. When the victim scheduler preempts or yields their current process and selects the next one from the queue, they can instead check for a thief process which may syphon a process away to spawn on the thief's home scheduler. This blocks the scheduler for

a longer period of time, but uses ErLam’s inter-scheduler message-queue rather than synchronizing on another LPU’s process queue. This secondary queue can be accessed less frequently and at the scheduler’s discretion so the synchronization overhead isn’t incurred upon standard usage (as no other scheduler can access the local process queue).

ErLam also comes with three cooperativity-conscious schedulers: the Longevity-Based Batching Scheduler (section 3.4.1), the Channel Pinning Scheduler (section 3.4.2), and the Bipartite Graph Aided Sorting Scheduler (section 3.4.3). The first two build on the same shared queue module as provided by *MTRRWS**, while the third utilizes its own implementation.

For any compiled ErLam script, the runtime installs a command line option for selecting the scheduler used (among several other options). We are able to specify that we wish to run *pfib*, for example, with *MTRRGQ* with the following command:

```
./pfib -s erlam_sched_global_queue
```

Any new schedulers can be added to the ErLam toolkit without needing to recompile the scripts as they are dynamically fetched and loaded at runtime.

3.3 Simulation & Visualization

The second primary goal of the ErLam toolkit was the ability to visualize how a scheduler proceeded to evaluate an ErLam application. We therefore needed a way to log all events over time, including unique per-scheduler events, such as the size of both the primary and secondary queues in the CML scheduler. It would also be advantageous to be as finely grained as possible and leave it up to the visualization mechanism to dial the accuracy.

We also needed a sample set of application simulations to run our set of schedulers against. These simulations needed to be minimal to reduce extraneous data but still demonstrate various levels of cooperativity and phase changes. We would like to also have the ability to compose test cases together to better create realistic work-sets for the schedulers to react to.

3.3.1 Runtime Log Reports

Logging in Erlang is a fairly simple matter. We utilize a simplistic data logging module based on syslog. The output of running an application could look like this:

```
timestamp,lpu,event,value
...
983847.935268,3,sched_state,running
983847.935333,0,queue_length,59
983847.935677,24,channel_blocked,6102
983847.935683,6,yield,""
983847.936003,4,queue_length,50
983847.936430,3,tick,""
983847.936439,3,reduction,""
...
```

The time-stamp given is a concatenation of the second and microsecond that the event happened in. The lpu is the scheduler which caused the event, unless it's a channel based event, such as a *channel_blocked* event, in which case it's the channel ID.

Our logging API is fairly simplistic as we only need to capture two types of metrics from our events: quantity and frequency. With frequency, we want to know the amount of events which happened in a time range, but with quantity we would like things like length of the scheduler's process queue over time or the amount of time spent in the running or waiting state.

Note time is not consistent per LPU, it may be the case that another OS application is getting time instead of one of the ErLam schedulers. This could result in one or more

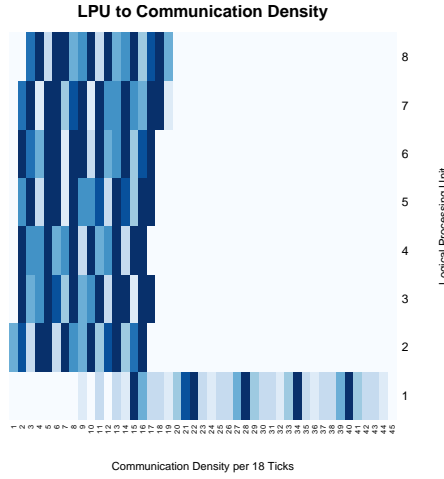


Figure 3.8: Example of Communication Density graph for the Work-Stealing scheduler on a Core i7 running the *PRing* (defined in section 3.3.2) application.

of the LPUs getting far less “*tick*” events. Worse yet, there may be a large gap of time missing from one scheduler to the next. For our purposes though we would like to compare the state of the scheduler while it is executing and would be fine with averaging over the largest gap. These from experimentation have not been found to be very frequent or large on an otherwise unoccupied processor (see section 4.1.2 for details).

To explain this averaging technique we’ll now discuss the report generation method. The ErLam toolkit comes with a secondary R script which can be given a generated log file for processing. This script dynamically loads chart creation scripts based on the types of events it sees in the log file. The toolkit comes with five charting scripts which should work for all schedulers: Channel Usage (Communication Density) over time, Channel State (blocked vs. unblocked) over time, Process Queue Length per LPU over time, Reductions (Computation Density) over time, and Scheduler State (running vs. waiting) over time.

Communication Density for example (see figure 3.8, creates a heatmap based on the frequency of *yield* events which occur whenever a process attempts a *swap*. Each cell of the heatmap is a color intensity based on the number of *yield* events seen in a given time-

slice for a given LPU. This time-slice is where the averages come into play. R heatmaps have a maximum of 9 colors, so any range we select must be scaled to 9. However, the constant multiplicand is based on the mean amount of time N ticks take place across each LPU. We can obviously tune the accuracy of these averages on a per-LPU basis by modifying N . Anecdotally, this turned out to be advantageous on several occasions when debugging scheduler implementations. As decreasing the number of ticks to average together, increased the number of samples and thus accuracy.

3.3.2 Cooperativity Testing

As part of the thought experiment, we needed to implement a decent set of test cases which would give us a good coverage of the range of cooperative behavior in common applications.

On one hand we have an axis depicting the amount of parallelism possible in an application. A system which is completely parallel, would be one where all processes spawned have no dependence on any of the others. For our toolkit, we called this behavior *ChugMachine_N* (figure 3.9(d)) ; where N depicts the number of parallel processes. On the other side of the axis, we would have a system which had absolutely no parallelism possible. We called this behavior *PRing_N* (figure 3.9(b)), as it would spawn N processes in a ring formation and pass a token in one direction. Each process has a channel to its left and right and would synchronize to the right until it receives a token to continue.

PRing_N also gives an example of full-system cooperation, except we would instead like some degree of parallelism possible. To experiment with that, we would need to throttle the degree of cooperativity. This behavior is called *ClusterComm_(N,M)* (figure 3.9(c)) as it spawns N processes and M channels which can be synchronized with by any process. Note for this system to work with swap channels we limit M to be at most $\lfloor N/2 \rfloor$ for all tests.

$ClusterComm_{(N,M)}$ is also an example of full-system cooperation, we also want to have a possible case for partial-system cooperation. We begin this range of experiments with a behavior which acts like a bunch of $ClusterComm_{(N,1)}$ running in parallel. We call this special case behavior $PTree_{(W,N)}$ (figure 3.9(a)); where W is the number of work groups to run in parallel. This is the cleanest case of partial-system cooperation. We would expect to observe obvious clustering of processes by work-group affiliation if the scheduler was cooperativity-conscious.

However, to expand on the concept of partial-system cooperativity, we would also like to experiment with lop-sided behaviors where a work-group exists along with other processes which may not be affiliated with one another. An application like this would be the combination of $ClusterComm_{(N,M)}$, $ChugMachine_N$, and/or $PRing_N$ running in parallel. For this reason, we made our behaviors composable.

We are missing two important behavior simulations: application phase changes, and hanging processes (typical of I/O bound processes). In the case of the latter, a simple built-in command *hang* was provided which would simulate hanging for a random amount of time before allowing the process to proceed with evaluation. If a scheduler attempted to reduce the process before the *hang* time was completed, it would be immediately preempted. The behavior which implements this is called $UserInput_{(T,C)}$ (figure 3.9(e)); where T is the max time in seconds the process would hang before continuing, and C is the number of times it would simulate “waiting for user input”. This simple behavior would also compose with the others.

For the former missing behavior, phase changes, we decided to make a variation of $PTree_{(W,N)}$ called $JumpShip_{(W,P)}$ (figure 3.9(f)) which would act like $PTree_{(W,N)}$ but would “change phase” P times before completion. The act of “changing phase” would be the successive relocation of all the processes from one work-group to another, effectively having all processes from work-group X “jump-ship” to $X + 1 \bmod N$.

We would have liked to possibly experiment with variations on the “jump-ship” behavior so as to inject phase changes into $PRing_N$ (by perhaps reversing direction) or $ClusterComm_{(N,M)}$ (by switching to $ChugMachine_N$ for a brief period before returning to $ClusterComm_{(N,M)}$). Yet time constraints have limited us to the aforementioned.

3.4 Cooperativity Mechanics

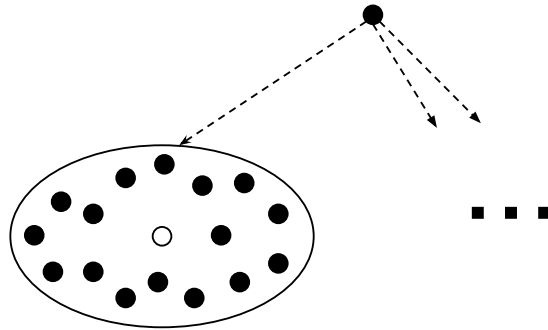
As mentioned previously, ErLam provides a number of feedback-enabled cooperativity-conscious schedulers for comparison purposes. Through our exploration of cooperative behavior we noticed that cooperativity has, on some level, been captured in previous scheduling systems. Through techniques keeping in mind cache locality, channel efficiency, and work-stealing efficiency, we see a common theme of keeping processes which communicate together, in close proximity.

Ultimately cooperativity gives us a mechanism for recognizing when processes may be moving along the spectrum of application parallelism. We would therefore like to look at systems which recognise particular behaviors which utilize this.

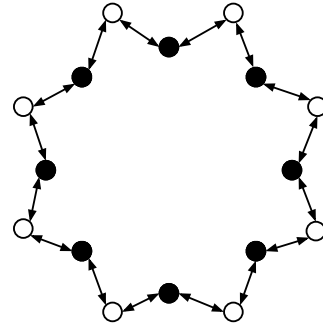
3.4.1 Longevity-Based Batching

As we’ve mentioned before, batching processes together has been a common mechanism in capturing some of the lost efficiency of cache locality. To take advantage of the cache a message passing language is required to be considerate of their channel implementation to allow for it. For example, *occam- π* makes sure their channel fits into a single 32 word cache line (technically two, as they have asymmetrical channels and split it based on send and receive).

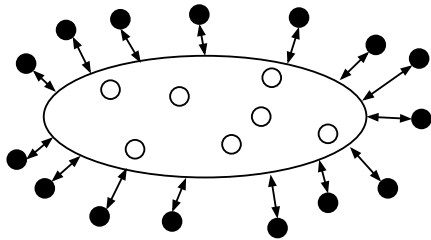
However, ErLam elevates the concerns of cache locality into an issue of process locality by relying only on symmetrical message passing. Note that a channel is loaded into memory at the location (*i.e.* LPU) of whichever process first blocks it and then again at



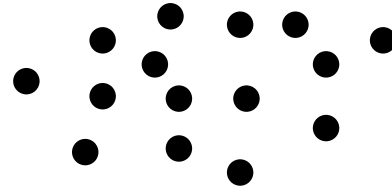
(a) Graphical representation of *PTree*, N Parallel work groups.



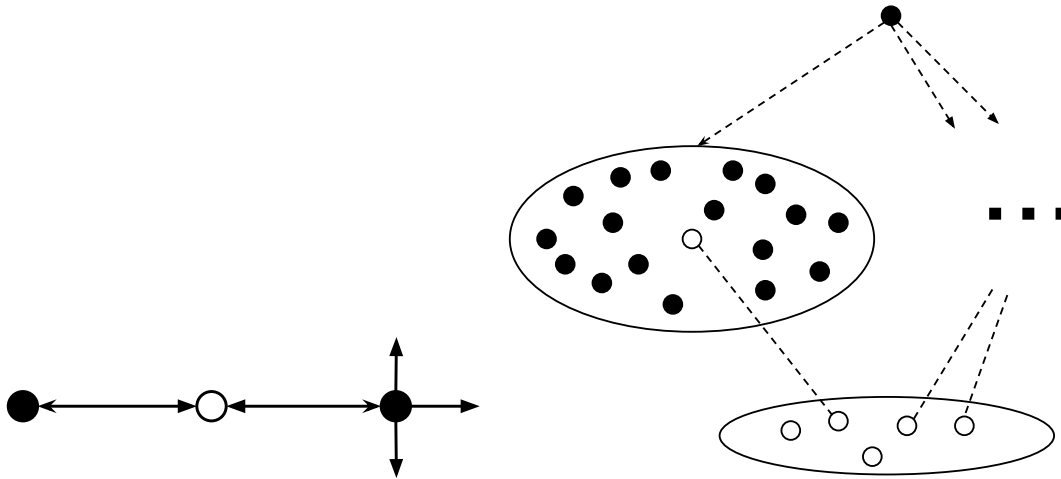
(b) Graphical representation of *PRing*, full system predictable co-operation.



(c) Graphical representation of *ClusterComm*, N processes to M channels for unpredictable full system cooperation.



(d) Graphical representation of *ChugMachine*, N worker processes without cooperation.



(e) Graphical representation of *UserInput*, single randomly hanging process.

(f) Graphical representation of *JumpShip*, N Parallel phase shifting work groups.

Figure 3.9: Simulated behavior examples and test primitives.

whichever process unblocks it. Thus to reduce the number of times the channel needs to be loaded, we can still use the batching mechanism. Namely, if two processes are communicating frequently, the channel never needs to be migrated if they exist in the same batch and thus on the same LPU. This mechanism would be applicable to any other language which uses message passing too.

However, this batching mechanism is great when the application is going through a phase of frequent communication and thus fine-grained parallelism. But in the event the phase changes or the system is coarse-grained, then a batch actually harms the ability of the scheduler to parallelize to the fullest. This issue however is one we've seen before, the struggle of computation and communication bound processes.

The CML Interactivity based scheduler breaks up the processes into groups of long running (computation-bound) and short running (communication-bound) processes. We can take advantage of this in our case by kicking processes which are too long-running out of a batch (into a singleton batch for example). To merge communicating processes back together in the same batch would be as easy as turning on Process Absorption for our channels. A process would then be batched with processes that have all begun communicating on a particular set of channels.

The mechanism we just described has been implemented as the Longevity-Based Batching Scheduler. A process belongs to a batch as long as it does not exceed its quantum (reduction count). If it is preempted, it is thusly kicked out from the batch it's a part of (unless it's a singleton). There are of course alternatives to this (*e.g.* allow for N quantum to pass before kicking a process from a batch), but we, for testing purposes, can just extend the size of the quantum for the same effect.

To gain entry back into a batch, all a process needs to do is perform a swap with Process Absorption turned on. Note as Process Absorption must be toggled on for this to work, we can experiment with max-parallelism possible in the application. Our assumption

prior to experimentation was that, the longevity-batching scheduler would drop-down into a common work-stealing scheduler in the worst case (*i.e.* all processes are singleton batches). We talk more on these assumptions and their validity in Chapter 4.

3.4.2 Channel Pinning

An alternative approach to batching, which moves the channels to the processes, is to set an affinity to a core based on which channels you cooperate on. We call this mechanism Channel Pinning, as we bind a channel upon creation to a particular LPU and force processes to that location to perform a swap. There are a large number of interesting mechanics for this behavior. But we will look at three: 1. How to spread the channel pinnings? 2. How should processes react when attempting a swap on a remotely bound channel? 3. And based on the decisions made in the previous, how should a scheduler steal/spawn a process?

When choosing a channel spread algorithm there are two things which need to be compared, cost of creating a channel and channel usage of the running application. In stark contrast to the previous scheduler, channel pinning would either need to be an expensive heuristic or a programmer-aided decision based on the application itself. For example, if our channel pinning algorithm was a sane even spread across all processors we would have ignored the possibility that a subset of the channels could be used more frequently. This could therefore cause more harm than good. If we chose to do an expensive check across all processors to compute the saturation each time we create a channel we would be harming the application which uses a map-reduce style, and uses a lot of temporary one-use channels.

On top of this complication, this also ignores the possibility of phase changes. It may be the case that during a start up phase, the frequency of particular channel usage may offset the saturation to such a degree that any further checks will suggest alternate

processors despite possibly being inaccurate. However, these issues are beyond the scope of this discussion and we will instead focus on scheduling around channel pinning. As such we only provide the following channel pinning implementations: 1. *same*, which pins the channel to the same processor it is created on (*i.e.* the processor which evaluates *newchan*). 2. *even*, which pins the channel in a round-robin fashion starting at LPU 0.

Note *same* pinning, would not be ideal for an application which creates all channels in one process and then hands them out to its children like in $JumpShip_{(W,P)}$, or $ClusterComm_{(N,M)}$. However, *even* pinning will be perfect for them. We expect experimentation with composed application will have interesting effects in this scheduler.

We have a couple of possible mechanisms for how a process should be handled when it comes time to communicate. In the event it is wanting to communicate on a channel that is not local, we propose the following mechanism: let them anyway but if they block, migrate them to the LPU which owns the channel.

The theory for this is, in the event of a block, the local LPU won't gain anything from having it in its queue (if process absorption is turned on it would lose it anyway). However, if the process completes a swap, both the remote LPU and the local can continue in parallel.

Due to this selective spawn feature, we have the opportunity to look at a selective steal opportunity. Namely, when stealing we can attempt to grab from a random scheduler, one or more processes which have communicated with a randomly selected channel which the thief owns. This is akin to the children's card game "Go Fish" where in our case a scheduler may ask if another "has any channel 3's".

However, with this mechanism it may be the case that there is never any work for a particular scheduler. In this case, we have added the post condition that if a process has never communicated, or if the scheduler is not the owner of a channel, then they act as wild

cards and can match anything. This is both a simplistic algorithm, but it also makes sure the scheduler falls back to a standard work-stealing algorithm by default.

We now note that the primary interest in comparing this scheduler is to look at this work-stealing mechanism. We would like to see how this type of mechanism fairs against both a best and worst case scenario. We first thought the worst case scenario would be a *ChugMachine_N* due to the reliance on wild-cards, however after more consideration a single *ClusterComm_{N,1}* may end up having a worse behavior due to the constant re-spawning of processes back to the owner of the primary channel. A best case in this scenario would be a *PTree_(W,N)* when W is greater than or equal to the number of processing units available.

3.4.3 Bipartite Graph Aided Sorting

Both of the previous schedulers ignore the effects that ordering can have on execution behavior. We hypothesize that order of process execution could have a drastic consequence on highly cooperative processes by pairing channels such that if a process were to block, the unblock would happen as soon as possible (*i.e.* the scheduler would not choose a process which had no probability of unblocking it).

Granted the extreme of this type of scheduler would be excruciatingly unfair, and as such we still maintain a round-robin like behavior, except now we rely on a sorted process queue. Sorting the process queue would allow us the chance to increase the likelihood of immediately unblocking a blocked channel, while still maintaining execution fairness. Sorting the process queue would also have the aided side effect of making work-stealing potentially more efficient when stealing more than one from a victim queue. Namely, it would be much more likely that the group of processes stolen are cooperating.

Due to this, we hope to show an interesting case where process absorption may not be the preferred channel implementation. The blocking channel, one which will im-

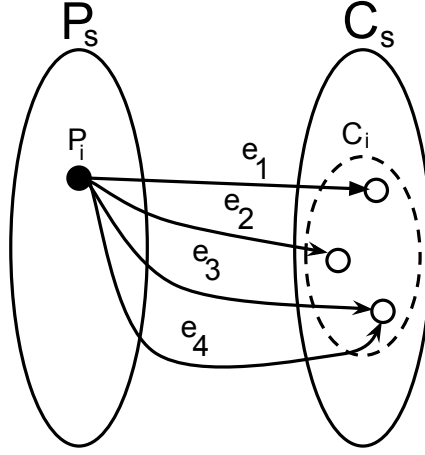


Figure 3.10: Bipartite Graph of Processes (P_s) to Channels (C_s).

mediately return, will allow for the process to stay sorted and allow for the next process following it to unblock it for its next turn. Thus relying on work-stealing alone to migrate processes in a potentially smarter and more-grouped way.

There are three metrics which could affect the order of processes and subsequently trigger a re-sorting of the process queue. A process yielding, returning, or spawned could all mean that a particular process or set of processes should be relocated. However, frequent sorting may cause the scheduler's fairness to suffer. We therefore set a variable, Γ which defines how many "events" can happen before causing a re-sort.

To provide a mechanism for sorting, we structure our process queue as a bipartite graph (figure 3.10), where one side is our process queue, and the other is the set of channels. We generate a "pseudo-priority" based on recency of the communications over the number of channels it has communicated with. Namely, we sort the process queue by $\Delta(P_i) = \Sigma E_i / |C_i|$ where E_i is the edge set (*i.e.* integer timestamps, e_j), and C_i is the set of channels P_i communicated with. The default priority function, Δ , is intended to give our processes which communicate frequently, a head start, and all other processes can be pushed to the

end (and more likely stolen).

If our process queue is long, we maintain preferential treatment for the set of interactive processes. If our process queue is short, then the effects of sorting are negligible. As such, we should only be concerned with sorting a process queue after a particular size. This provides yet another opportunity for heuristical analysis. Due to this though, we may expect to see worse behavior on most of our simple test primitives, while on more advanced applications these behaviors could bring much improvement.

Chapter 4

Results and Discussion

4.1 Evaluation

We begin our discussion by first stepping back and performing a meta-validation of the ErLam toolkit itself, before comparing the scheduling mechanisms amongst themselves. We ask:

- How complex must our implementations be to create our test primitives?
- For each scheduler, does our implementation work as expected, despite a minimalistic scheduler API?
- Does the channel implementation lend itself to scheduling decisions? If so, in what cases?

These questions will evaluate ErLam, both as a language and runtime, but also as a simulator for scheduler experimentation, comparison, and evaluation. We attend to these questions in order; Section 4.1.1 critiques the language as a medium for simulation design through the development of our test primitives. Section 4.1.2 discusses our evaluation of the scheduler API using several of the testing primitives on the aforementioned classical schedulers. Then in section 4.1.3, we discuss our findings regarding channel implementation differences and their subsequent effects on scheduling behavior.

All testing was done on a Ubuntu 12.10 machine with Linux kernel version 3.5.0. The machine has 8GB of RAM and the processor was an Intel Core i7-2820QM at 2.3 GHz, which has 4 cores with HyperThreading. This puts the number of logical processing units

```

merge = fun a.(let x = newchan in
               let _ = (spawn fun _.(swap x (a nil))) in
               fun b.(let y = newchan in
                      let _ = (spawn fun _.(swap y (b nil))) in
                      fun m.(m (swap x nil) (swap y nil))))
// ...
(omega fun f,n.(if (leq n 1)
                  (worker_t nil)
                  (merge fun _.(f f (dec n)) worker_t ignore))
 N)

```

Figure 4.1: Our implementation for a map-reduce style fork-branch, and its subsequent standardized usage.

(LPUs) at 8 for all multi-threaded scheduling tests. The test machine ran the ErLam runtime on the 64-bit Erlang R15B03 VM with SMP turned on for all tests.

4.1.1 Test Case Implementation

Our intentions when choosing our base language constructs were primarily focused on simplifying the base language. This minimalism we hoped would remove any noise which may be caused by the implementation details. We hoped to make, for lack of a better term, a concurrent functional assembly language. As such, there was some concern as to the level of ease we would be able to implement our testing primitives.

We start with a critique of the first test case implemented: *ChugMachine_N*. It soon became apparent that we would need to standardize on a successive spawning method for producing several of the same worker process at once. Rather than modifying our *spawn* expression, we decided to implement this as a function in the language as it is. The reasoning behind this was to keep consistent the effects of spawning a process into a process queue. If we allowed the process to produce *N* processes at once, the lag caused by the run-time to produce them would be more noticeable as the time between ticks would increase.

We settled on the *merge* function (figure 4.1), along with a standard method of invoking it. Our *omega* function would enable recursion, for us to use one branch of the

merge call to create a left-loaded binary tree. This would keep a consistent behavior despite a bias towards the initial processes. Note that a work-stealing or global-queue scheduler would gain access to the most recent processes sooner (and would thusly get a chance to reduce sooner).

Also as a side effect of our decision, there is a channel for every process which immediately blocks. This has the added effect of giving us the ability to track when processes terminate, as the long-term blocked channels will start closing. For example, figure 4.2, gives an example parallel Fibonacci program written in this style and its subsequent channel graph. This is reminiscent of a map-reduce style approach, and the language lends itself to it. Note on the channel graph, a dark line indicates a channel which becomes blocked until the point at which it becomes unblocked.

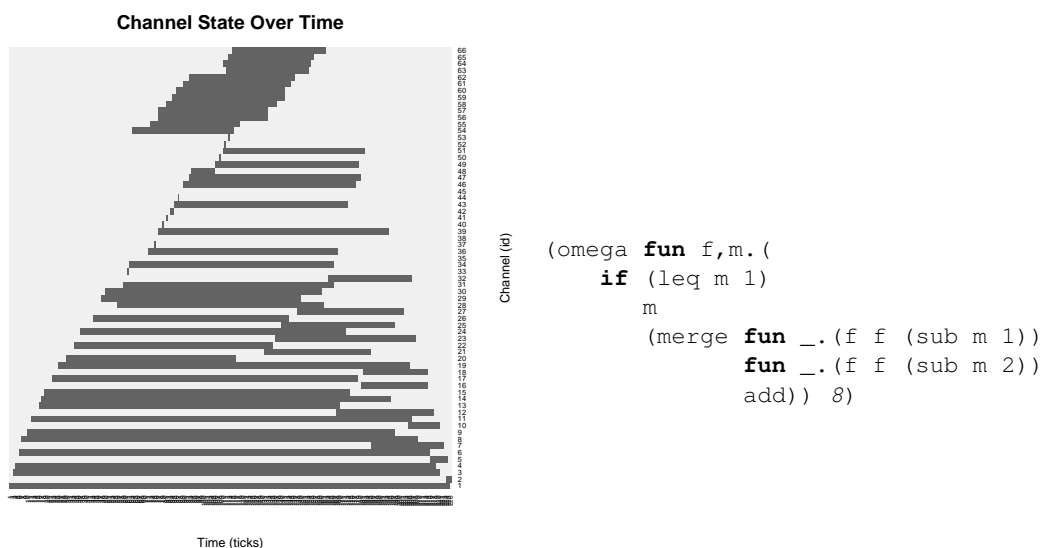


Figure 4.2: Parallel Fibonacci implementation and a potential channel graph.

For example, note the bottom three lines. The first one and the third one are all dark until the very end. That first one is the block on the left-hand side of the Fibonacci merge call, and the third is the response from its subsequent spawns. The second is never blocked

until the end, after we hear back from the left-hand side. These types of observations will become useful for the later scheduler critiques.

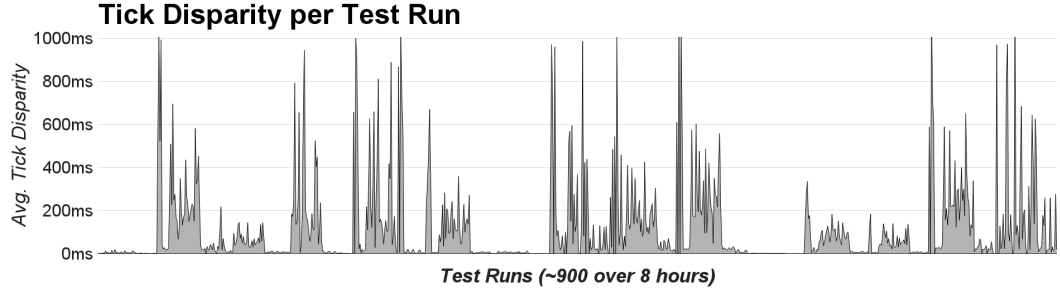
4.1.2 Scheduler API

The ErLam Scheduler API was minimally constructed around the single-step scheduling semantics presented earlier in section 2.2. We were motivated by the simplicity of the description and thus the ability to bring some consistency to the implementations. That being said, we would still require the option to observe practical statistics such as runtime overhead during our scheduler comparisons.

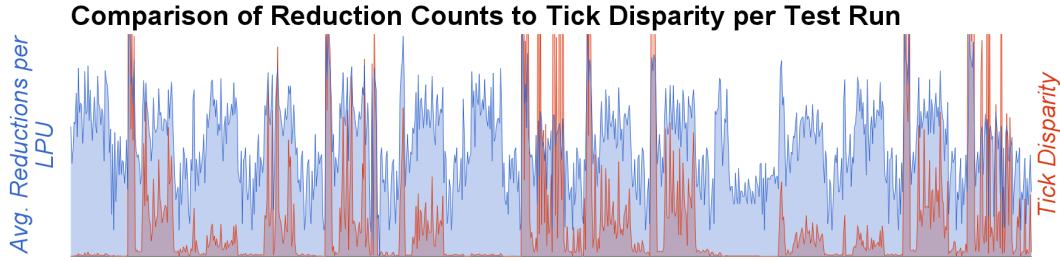
However, one of the concerns early on, and as described in section 3.3.1, was that the LPUs could greatly differ in the number of ticks they are able to provide their process queue. This could be caused by the OS preempting the ErLam scheduling thread to execute something else. However, from our tests, we found these gaps to be minimal and mainly caused by the scheduling implementation itself.

Figure 4.3(a) shows the average time between ticks averaged over the LPUs for a large subsection of the tests we ran. We call this average time between ticks per LPU the Tick Disparity of the test. From the figure, we notice obvious clustering, and figure 4.3(b) leads us to believe this is primarily caused by spikes in reduction counts. A scheduler chugging on processes the entire time, must wait until preemption in-order to handle any inter-scheduler communication, as in the case of work-stealing schedulers. Thus, for our purposes, all talk of tick-disparity could be considered a discussion of scheduler overhead. As such, comparative analysis of scheduler implementation overhead from test case execution would still be a valid comparison with our design.

Further, to validate that our scheduler implementations functioned as expected after translation was another concern. We gave the example of the CML Dual-Queue translation (*STDQ*) previously in section 3.2.4. We will now examine its execution and compare it to



(a) Tick disparity, or the average time between scheduler ticks per LPU, per Task run. Typical ranges tend to spike in groups, which show consistency based on scheduler implementation, rather than OS intervention.



(b) Tick disparity consistently matches up with increased computation, which is indicative of inter-scheduler communication requirements.

Figure 4.3: The tick-disparity for over nearly 900 tests.

STRR, a single queue naive scheduler to confirm our understanding.

The key differences we would expect to see in a comparison would be that the CML scheduler prefers to, and attempts to run the interactive processes first. It would push all computational processes onto the secondary queue, only promoting them when the need arises. To observe this property, we composed the $UserInput_{(T,C)}$ test with $ChugMachine_N$ to create an interactivity test primitive $Interactivity_{(N,M)}$ (figure 1.6). The primitive launches $ChugMachine_N$ and M instances of $UserInput_{(5,10)}$ ¹. We then subsequently ran this primitive with *STRR* and *STDQ* using multiple values for N and M . Table 4.1 compares an instance of this test set: $Interactivity_{(8,16)}$.

¹The values of which were arbitrarily chosen so as to execute for long enough to collect coherent data.

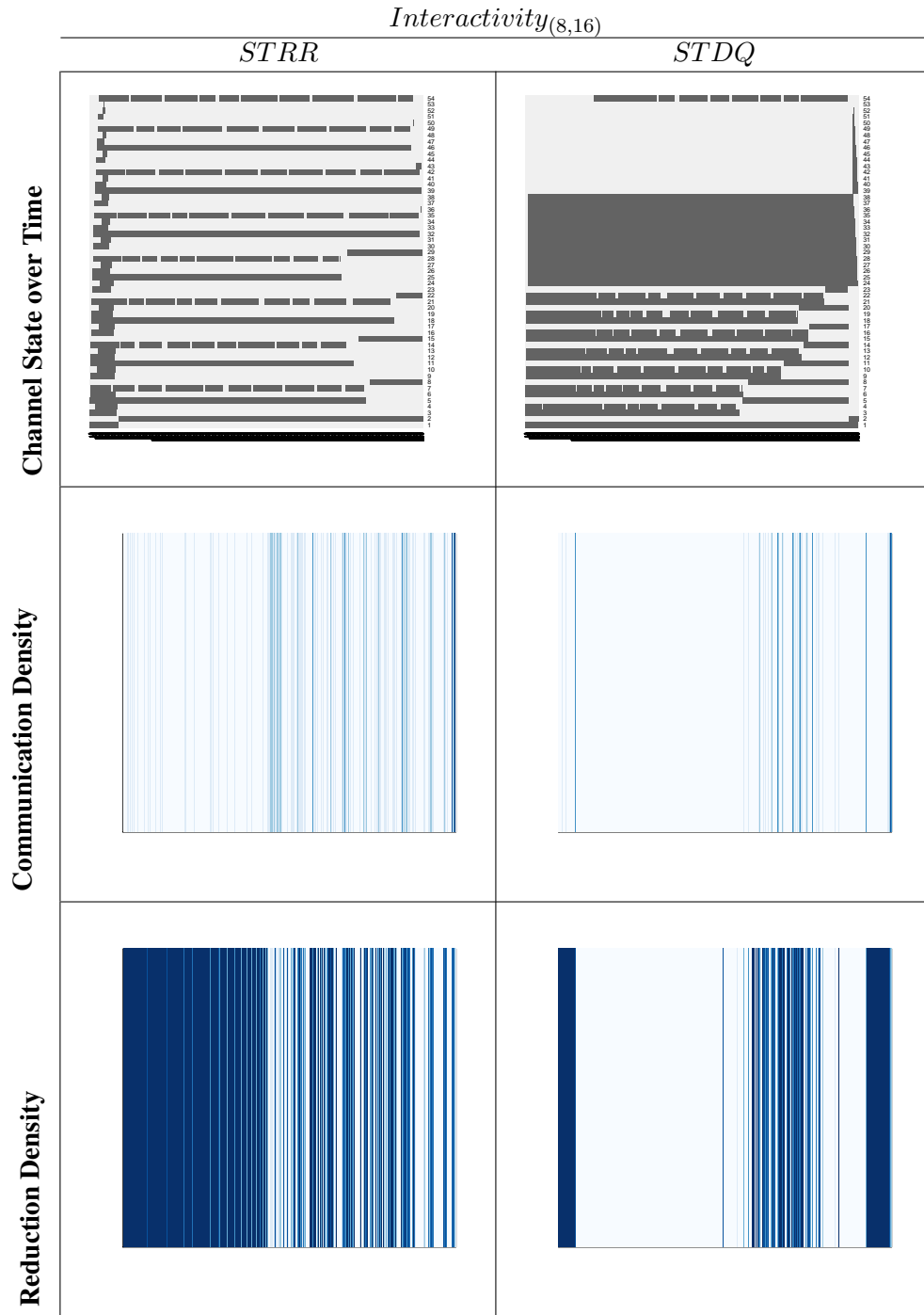


Table 4.1: Comparison of $STRR$ and $STDQ$ using Channel State, Reduction and Communication Densities.

The Reduction Density graphs explain everything we need to know. The darkened portions at the beginning of the chart indicate that the *STRR* scheduler has no regard for the interactive processes, whereas the *STDQ* scheduler remains spread out. This seems to be the opposite if we were to consult the Communication Density charts though. It would appear here that *STRR* is getting more frequent communication, and *STDQ* condenses all synchronizations to the end. This is, however, an issue of verbiage, as Interactivity does not correlate to Communication Density. By this, we mean, *STRR* is indeed allowing communication to happen evenly due to a round-robin schedule, but it is not attending to spontaneous events (*i.e.* responses back from *hang*). *STDQ* on the other-hand pushes all inter-process communication backwards as it waits to respond to the *hanging* processes.

We see this effect more clearly once we compare with the Channel State charts. In *STRR* we see an even regard for all processes. The tapering of the channel blocks at the beginning of the graph is consistent with our understanding of the *merge*-based successive spawning and the round-robin nature of *STRR*. However, *STDQ* is completely different, and all $UserInput_{(T,C)}$ processes seem to be pushed to the beginning of the execution pool. This is in-fact due to another difference between the execution styles of the two schedulers: *STDQ* replaces the currently running process with the child process it spawned, enqueueing the parent. We therefore can confirm our understanding, in this case, that the scheduler is reacting to the test primitives as expected.

The work-stealing mechanics of *MTRRWS-SQ* and *MTRRWS-IS* are another instance for interesting comparison. We've built two of the cooperativity-conscious schedulers we discuss on top of the *MTRRWS*-* process queue implementation. It is this unified queue implementation which allows us to toggle between the two implementations. With *MTRRWS-SQ*, the process queue will respond to function calls from schedulers for other LPUs, it will perform a quick dequeue from the bottom, only blocking the host LPU from accessing the queue for a minimal amount of time. With *MTRRWS-IS*, the process

queue goes untouched by other LPUs, instead the scheduler on the LPU itself will receive the thief messages and respond during periods between process operations (*e.g.* during pre-emption).

To compare these, we would expect to see minor differences in the processes scheduler state, but we could expect *MTRRWS-IS* to wait longer for responses from their thief messages as the victim LPU could be computation-bound. But otherwise we would like to make sure the saturation of the cores are as identical as possible. In other words, if either method is unable to saturate the cores effectually, then it would not be worth further testing (or there is an issue with the implementation).

However, work-stealing schedulers work best if there is a constant probability of loosing a process by completion. Our test primitives, as they stand, do not account for application phases in terms of process quantity or differing longevity. Thus, to effectively examine these stealing mechanisms, we substitute process loss via completion with process loss via channel absorption. Thus, table 4.2 gives a comparison of the two work-stealing techniques in terms of scheduler state over time and the process queue length with a run of *PTree*_(9,10) with the Process Absorption channel turned on.

Upon review of this comparison, our initial assumptions have been validated. The process queue length looks extremely similar. The LPU which owns the initial process maintains the largest queue, where all others work steal a single process and work on it. Any gains in queue size for these LPUs are via channel absorption, but this can not compare with the initial spawns. We note here that an obvious introduction of smarter work-balancing would be critical for a realistic or even practically useful scheduler. However, for our purposes of verifying the work-stealing mechanism and any improvements, this naive implementation is sufficient.

Although the scheduler state comparison, in this instance, does not look consistent with the original assumptions. Namely, *MTRRWS-IS* seems not stay in waiting mode

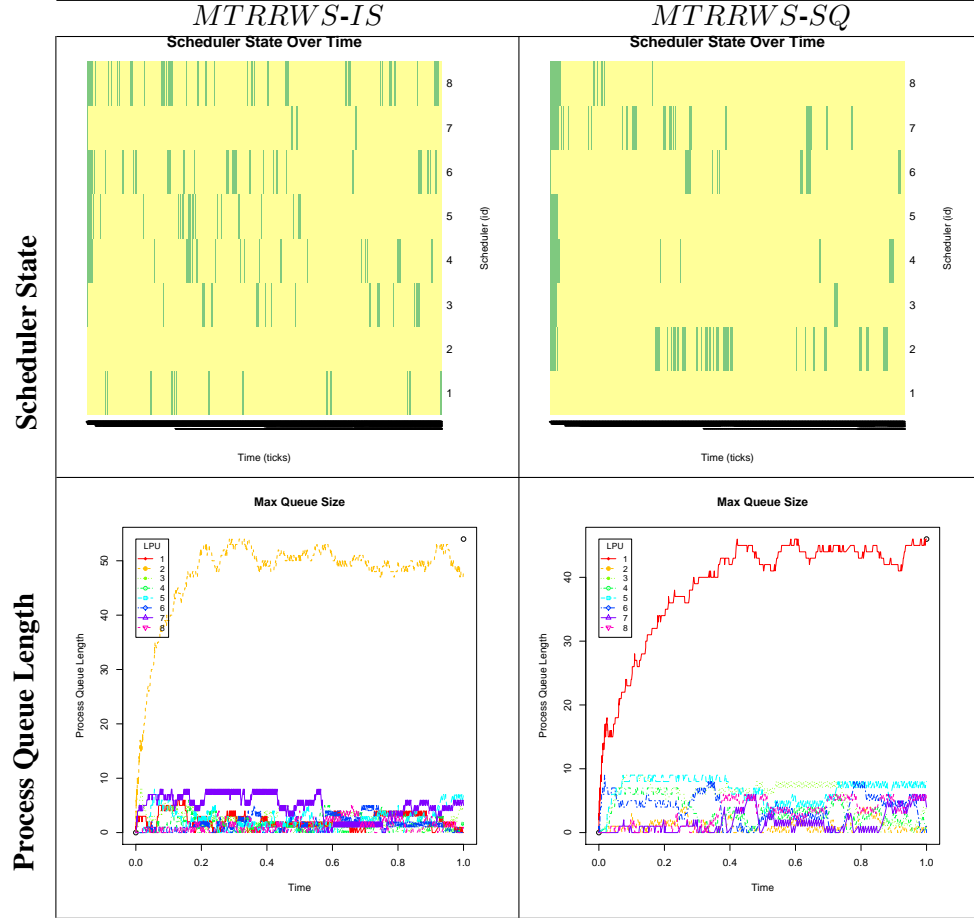


Table 4.2: Comparison of *MTRRWS-IS* and *MTRRWS-SQ* using Scheduler State and Queue Size.

as long as *MTRRWS-SQ* does. However, we can pose two possible reasons for this reaction: there is an obvious scaling bias introduced by squeezing over 11000 ticks into the same length of space as *MTRRWS-SQ* does with 8000. Additionally, the scheduling reduction quantum for this execution was 20 and all processes were set to chug for only 5 max, this allows the scheduler more opportunity to tend to inter-process communication at a detriment to the shared-queue implementation due to higher chance of blocked queue access. The simulation's log was able to support the former rational. It is due to this minor increase in speed, that we standardize our tests in this document to compare against the *SQ* stealing mechanism, even if our selected tests showed *IS* as preferred.

4.1.3 Channel Implementations

The above graphs (*e.g.* table 4.1, and table 4.2) were generated, using the CML-like Process Absorption channels. We would like to now briefly visualize how a Blocking channel may operate and effect our visualizations of the application's schedule. In this example (table 4.3), we ran the interactivity application again to test a large sampling of processes, but some with communication so as to see the flow of processes due to absorption.

Note the effects of blocking on the apparent communication density of the application. Instead of visualizing only the completion of a swap, this chart shows any attempted communication, even if it results in a block. Thus if a process is allowed to continuously check a required channel for a value, the density will increase, as it does for the Blocking channel chart.

The difference is also apparent in the number of available processes each scheduler has access to. The lengths of the process queues, fluctuate frequently in both charts, however the absorption channel does so to quite a large degree. This is to be expected since any communication which results in a block removes a process and any which results in a value returns an extra one. Whereas the blocking channel chart, the queue size rapidly switches

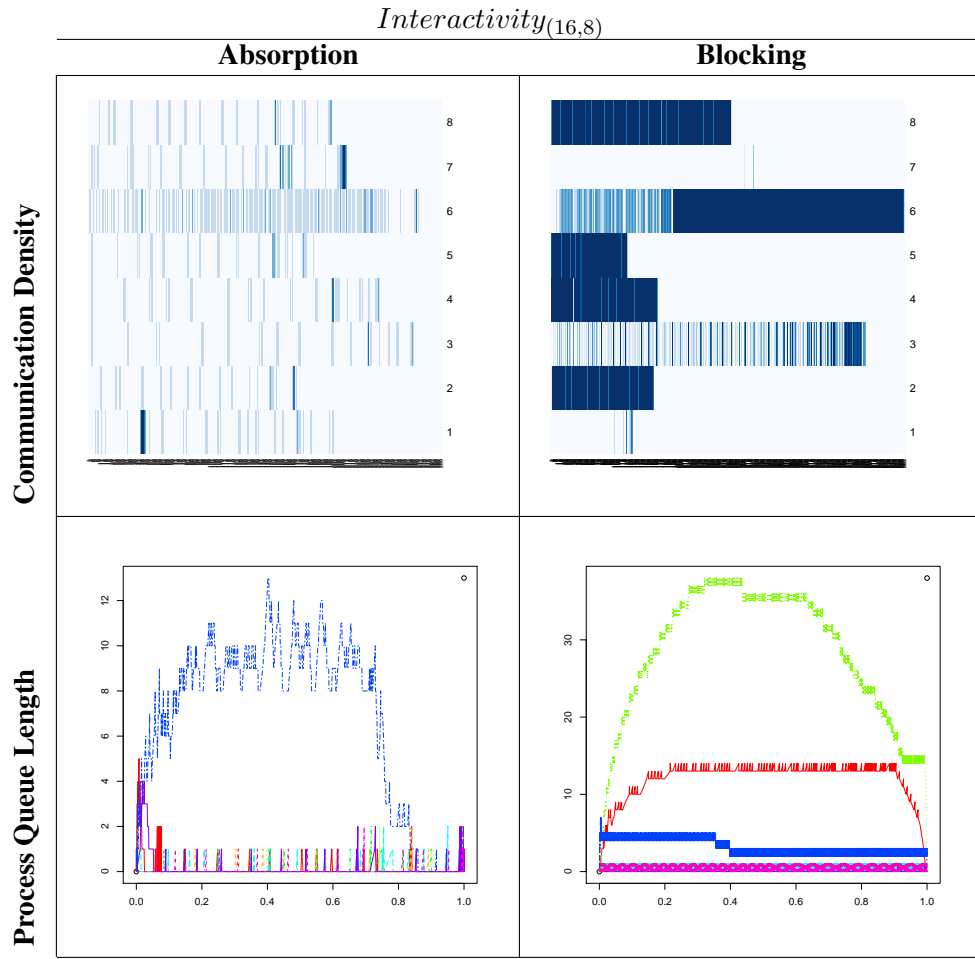


Table 4.3: Comparison of two runs of $Interactivity_{(16,8)}$ on $MTRRWS-SQ$ using either Absorption or Blocking swap channels.

up and down as it adds its current process and gets the next one. We are also able to notice the tell-tail signs of the naive shared-queue work-stealing approach in the Blocking channel too (*i.e.* the single dominate process queue).

We discuss these channel differences further in the next section as we compare the cooperativity-conscious schedulers. At this point the operation of the blocking scheduler seems unmotivated, however, its effects on the Bipartite-Graph Aided Sorting scheduler support the additional study.

4.2 Cooperativity Mechanics

We now turn to the discussion of cooperativity-conscious scheduling. Using our classical scheduler results as a base line we can evaluate the presented scheduler mechanics independently. This will give an indication of what may warrant further investigation, and testing. We direct the readers attention to appendix 1 for full source descriptions of the test primitives used throughout this discussion. For any non-explicitly defined parameter selections, the appendix will have their default values.

However, with the mechanics presented in this section, we wish to highlight particular distinguishing features of using cooperativity as a metric in a feedback scheduler. Namely,

- The Longevity-Based Batching scheduler recognizes system interactivity and channel communication to batch processes, and longevity detection to split them. As such, it is an experiment in using process communication behavior to better adapt the system to the level of cooperativity.
- The Channel Pinning scheduler is motivation for further examination of the work-stealing mechanism. It looks to adapt to the dynamic nature of cooperativity by spreading the processes based on common synchronization points.

- The Bipartite-Graph Aided Sorting scheduler is motivation to take another look at the channel implementation. Instead of the common absorption channels, a blocking channel allows for scheduler decisions regarding order of process evaluation. This allows for adaptation to possible future cooperative behavior.

We therefore restrict the scope of our discussion to the particulars of each mechanic which directly pertain to cooperativity recognition and adaptation. We leave it for future work to improve on the trade offs of these mechanics in practice.

4.2.1 Longevity-Based Batching

The Longevity-Based Batcher was an experiment of worst-case scenarios. We mention in section 3.4.1, that a worst case scenario would be one where all processes were seen as long running and thus incapable of being batched. Our desire was for it to degrade into a common work-stealing mechanism in these scenarios, and this should hold despite the channel implementation or time quantum.

We would also like to look at how it would react to a $PRing_N$ execution. This would be an optimal case as it would batch all processes up to the point where N is larger than the batch cut-off. Effectively, the longevity batcher would act like a single threaded scheduler and forgo having to communicate across processors. Subsequently, we would be able to verify a positive result for this, given communication and reduction density charts. If a single LPU were completely saturated while all others were kept in waiting mode, this would indicate the batching process was a success.

We also would like to look at a number of other possible tests to see the average cases and how a batching scheduler would handle such applications. Namely, various $ClusterComm_{(N,M)}$ and $Ptree_{(W,N)}$ executions would allow for testing of process-absorption based re-batching. We would expect logical work-groups to form batches, and

only show poorer results in instances where the worker thread computes for longer than the time quantum (as they would be repeatedly kicked from the batches). Therefore we will instead look at the effects varying the time-quantum has on the quality of the batching procedure. We will then look at the process queue lengths and work loads of each LPU.

4.2.1.1 Boundary-Case Scenarios

We will first consider the worst-case scenario of all computation-bound processes, by using *ChugMachine_N* for several values of N near and above P , or the max number of LPUs on the machine, which in our case is 8. We will compare it to the graphs of *MTRRWS-SQ* to determine how close to naive work-stealing our implementation achieves. We will then subsequently consider the case of *PRing_N* for various values of N surrounding the batch size, B , to verify our absorption and batching assumptions.

First, table 4.4 visualizes a *ChugMachine_N* comparison with the Longevity-Based Batchter and *MTRRWS-SQ*. With the Longevity-Batcher, we can observe the common work-stealing behavior with a primary LPU and minimal queue lengths on all others. It initially spreads work out as the primary queue continues to chug. However, the *MTRRWS-SQ* seem to have an easier time of stealing the primary process which is in the process of spawning the worker threads. Therefore the spread seems to be consistently more even.

We hypothesized that the mechanism we were using for spawning into a batch was somehow making it improbable that it would be migrated to another core. In fact there are two mechanisms we have implemented for Long. Batchter to handle a spawn.

- In ‘singleton’ batching mode, if a spawn would cause the current batch to be $\geq B$ then a new batch is created for the child and set as the current batch. The old batch with the parent is placed on the queue.
- In ‘push-back’ batching mode, if a spawn forces a new batch to be made, we push

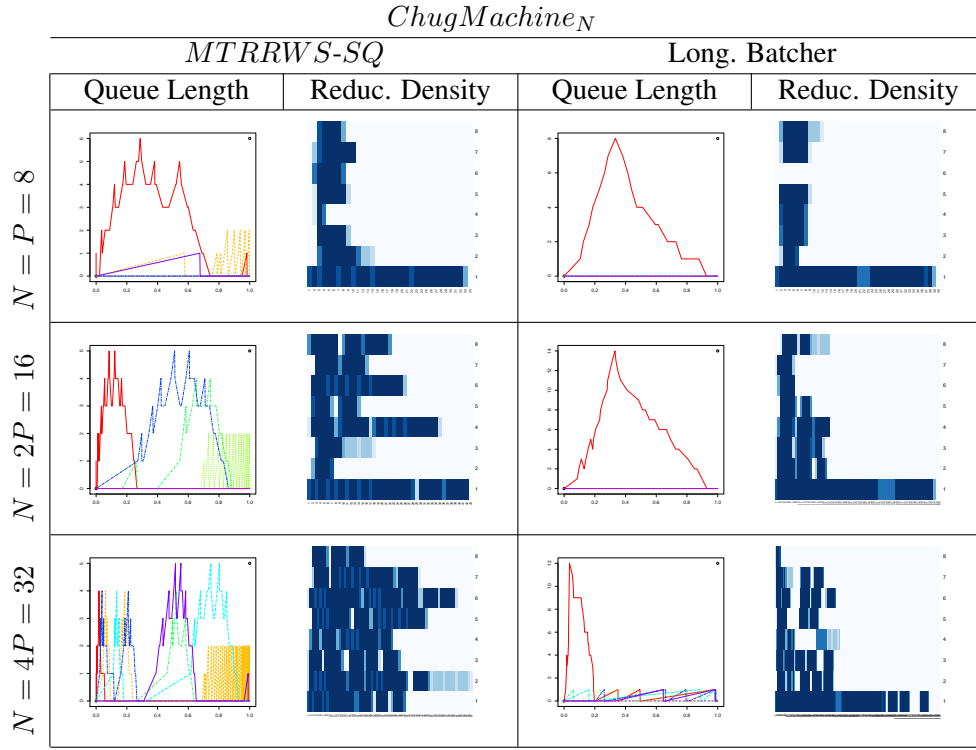


Table 4.4: Comparison of $ChugMachine_N$ spread on the Longevity-Batching Scheduler and $MTRRWS-SQ$.

the current batch onto the queue and keep the current process in the same batch as the new child. This will keep the parent close to its immediate child and also allow it to be quickly re-tested for more new spawns.

Note that in ‘push-back’ mode, the scheduler with the primary process will never be in a position to give it up until all children are finished being spawned. This was our default for the Longevity Batcher in the above test. As such, table 4.5 shows a re-execution of $PRing_{32}$ with ‘singleton’-mode turned on.

Despite giving it every opportunity to increase the spread, the parent process is a short-running process (as it would spawn a new process before it would hit its time quan-

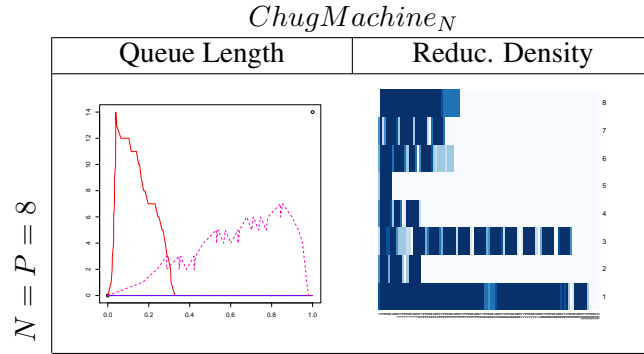


Table 4.5: Re-run of *ChugMachine_N* with Longevity-Batching Scheduler in ‘singleton’ batching mode.

tum). Therefore, while it does have a higher chance to get stolen, it will also have a higher chance of staying batched until it reaches the point where it hangs for completion. This obviously limits our chances at immediate parallelism. The conclusion that Longevity-Based Batching is *MTRRWS-SQ* in the worst-case scenario is therefore not quite true.

However, that being said, the spawning mechanism differences do point to some interesting application behaviors. Namely, while the ‘singleton’-mode was advantageous for this application start up, it may not be for all. For example, a program which rapidly spawns micro processes to get multiple small tasks done, may find the ‘push-back’ batching mode to be preferred.

Finally, table 4.6 visualizes the reduction densities of several *PRing_N* executions using the Longevity-Based Batcher with Absorption Channels turned on. We note the obvious agreement with our assumption as the entire *PRing_N* gets absorbed into a single batch and contained to a single core. The spill-over at the beginning of the *PRing₂₀* execution was due to the spawning of more than B processes. However, after their absorption via communication, the Longevity-Based Batcher was free to maintain all processes in the same batch without harming parallelism.

While the scheduler does not reduce exactly to *MTRRWS-** as hypothesized, this

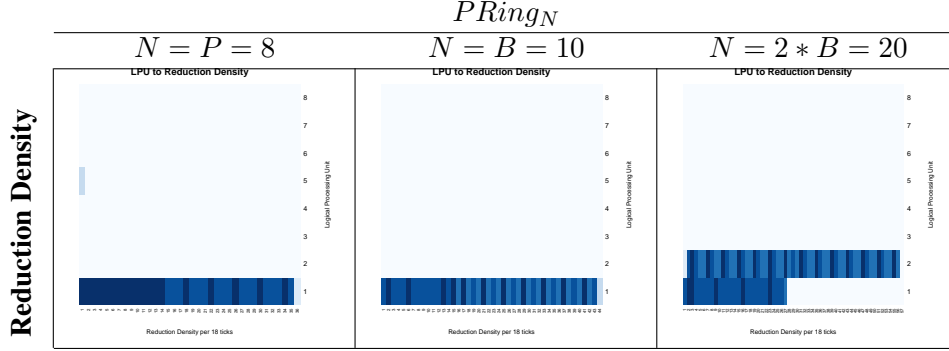


Table 4.6: Comparison of different sized $PRing_N$ on the Longevity Batching Scheduler with batch size $B = 10$.

result shows an improvement over alternative batching approaches as we explicitly relax the constraints of system-wide batching. Any lack of parallelism is caught by the check for longevity.

4.2.1.2 Time-Quantum Effects

Of the average scenarios we are interested in only the effects of our time quantum on the quality and level of batching in each scenario; as batching has been examined before in terms of alternative metrics. We will look at the same test case, $Ptree_{(4,8)}$ as the number of work-groups (4) is less than the number of LPUs and the number of processes per work-group (8) is lower than the batch-size limits (10).

The rational for this seemingly arbitrary test-case selection is two fold. First, the supervisory processes are all short-running and may force the logical batches to be inflated in size by at least two (*i.e.* thus the reduction in processes in work-groups). Then secondly, when we reduce the time quantum to well below the worker-thread's execution spawn (before it hits a sync), the number of batches will increase substantially and we would like to see the scheduling effects. Having a 2-to-1 number of LPUs to potential batches seemed logical.

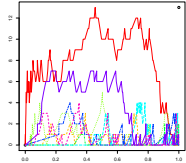
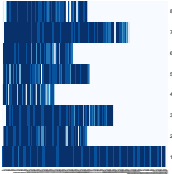
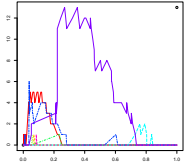
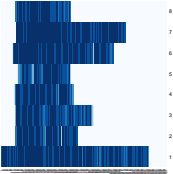
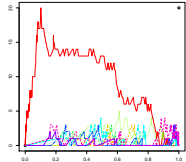
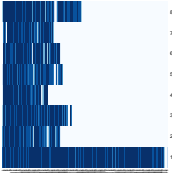
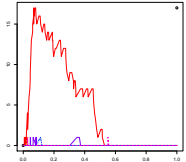
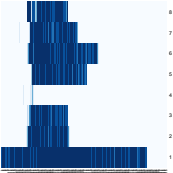
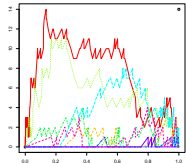
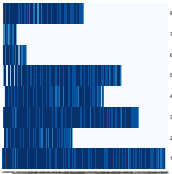
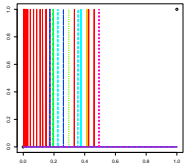
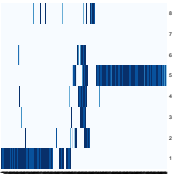
Time Quantum	$P\mathit{Tree}_{(4,8)}$			
	$M\mathit{TRRWS}\text{-}SQ$		Long. Batcher	
	Chan. State	Reduc. Density	Chan. State	Reduc. Density
$R = 30$				
$R = 40$				
$R = 50$				

Table 4.7: Comparison of $P\mathit{Tree}_{(4,8)}$ running with the Longevity-Based Batching Scheduler and $M\mathit{TRRWS}\text{-}SQ$ at different time-quantums.

Table 4.7 notes the gradual shift of the number of logical batches formed in the Longevity-Based Batcher. For the lower values of R , the max reduction count per round, the number of batches rose substantially and began to look like *MTRRWS-SQ*. For all subsequent quantum selections there was a steady drop in the sizes of the LPUs' process queues, until at $R = 50$ we see that no queue goes above size 1, indicating each work-group has been clustered.

We close on the note that the selection of the time-quantum can easily be set heuristically, modified between rounds, and possibly on a per-LPU basis. It would need to take into account the average distance between synchronization points of the processes in its batch queue. Using that, it would be able to determine if a particular process posed a greater than average threat to the parallelism of the system, and subsequently push it into a singleton batch.

4.2.2 Channel Pinning

This cooperativity-conscious scheduler attempts to group processes based on the channels they communicate frequently with. It does so by pinning the channels to the set of LPUs based on a particular spreading algorithm, and then piggy backs on the channel implementation to relocate the processes using absorption. This has the potential effect of compensating for frequent, and especially uniform, communicating processes ahead of time.

As mentioned in section 3.4.2, our primary interest in this scheduler is the selective stealing technique used along with its ability to maintain LPU saturation with channel pinning and process absorption. Its design is based on recognizing cooperativity where it exists. If two processes communicate with different channels, their likelihood of being reliant on one another decreases. As such, we will look at the two extremes for communication behavior, completely random and then completely uniform. That is, a system which has

no predicable or repeatable behavior, and then a system were the past will directly inform the future.

We therefore utilize $ClusterComm_{(N,M)}$ for its random behavior, and turn to the $Interactivity_{(N,0)}$ composition to simulate uniform communication. In other words, we will have N uniform communicating units which compute and are stolen separately ($UserInput_{(T,C)}$). As such, we hope to highlight the scheduler's ability to consistently saturate the cores with work under both the best (uniform) and worst case (random) conditions. We leave it to future research for a discussion of the possible channel spreading techniques and work-stealing mechanisms, along with their effects in practical feedback schedulers.

4.2.2.1 Random and Uniform Communication

We begin our communication behavior tests by observing the queue lengths and reduction densities during the execution of a particular pair of tests: $Interactivity_{(20,0)}$ and $ClusterComm_{(20,5)}$.² We subsequently compare results of the Channel Pinning scheduler with those of $MTRRWS-SQ$ via table 4.8.

We will start our discussion with the executions of $Interactivity_{(20,0)}$. Starting at the bottom and working up, we notice the level of saturation the channel pinning scheduler achieves over $MTRRWS-SQ$. Its slightly denser, despite the same general execution time (in tick quantity). However, the channel pinning scheduler still maintains the tell-tail signs of the underlying naive work-stealing approach via the single primary LPU. The queue lengths' confirm this with much more activity on all secondary LPUs. Despite the unevenness, we see a much uniform attention to the state of channels.

However, for $ClusterComm_{(20,5)}$ we see no marked difference in the channel pin-

²We chose these tests due to being in the upper-bound of process quantity of the tests we ran. They would be the best bet to demonstrate saturation.

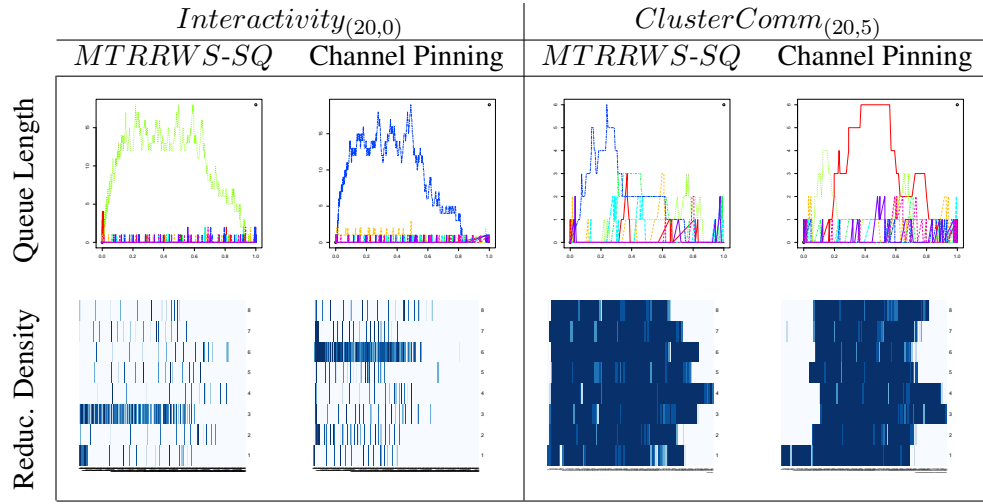


Table 4.8: Comparison of Random and Uniform synchronization for *MTRRWS-SQ* and the Channel Pinning Scheduler on Absorption Channels.

ning scheduler’s spread. In fact it looks to take longer to saturate the cores than a normal work-stealer. Possibly due to the work-stealing by channel-id not matching a process until much later. Despite this, it looks as though there was very little detriment, as the applications completed in roughly similar time.

Note for these tests we used the *even* spreading algorithm (section 3.4.2) to calculate our next LPU for pinning instead of *same*. Our $ClusterComm_{(N,M)}$ test primitive creates all channels in the same process before spawning all workers, so pinning all channels to the primary LPU would have been a detriment. Thus to give the tests the best possible chance at succeeding, we chose our most efficient spreading algorithm.

4.2.3 Bipartite-Graph Aided Sorting

The Bipartite-Graph Aided Sorting Scheduler attempts to observe the effects process queue order has on the scheduling of an application. It utilizes a mapping of the process set to the channel set and sorts its process queue based on the known edges between the sets.

With this scheduler, our primary focus was in the recognition of cases which lend themselves to sorting, as such we leave a critique of sorting algorithms and sorting frequencies to future work in this field.

We utilize the same tests as performed in the previous section, to visualize the scheduling differences of random and uniform communication behaviors. Although, the Sorting scheduler was the most complicated of the cooperativity conscious schedulers to test and evaluate. In fact, we needed to stray from our test primitives and examine one of our original example applications, the Parallel Fibonacci, to properly test it. This is due to none of our tests being dominantly dependent on process order (*i.e.* the majority of processes in the test primitives are worker threads and only 1 process is waiting on them to complete), thus Parallel Fibonacci was poised to be a best-case scenario.

4.2.3.1 Random and Uniform Communication

We ran $Interactivity_{(20,0)}$ and $ClusterComm_{(20,5)}$ like before, using Absorption channels first. We hypothesized that this channel implementation would provide little improvement and the overhead involved with sorting the process queue would be wasted as soon as a channel was absorbed and effectively reordered. Table 4.9 presents these executions, but they don't all meet expectations.

Note very little difference in the charts for either scheduler. However we must point out the discrepancy in our hypothesis in terms of the Sorting Scheduler's $ClusterComm_{(20,5)}$ run. This execution was executed in far less ticks than any of the other runs performed by $MTRRWS-SQ$. While we cannot explain the positive reaction in this case, we must recognize the random nature of $ClusterComm_{(N,M)}$ and the possibility of repeat channel accesses when M is low. We would also like to note here, that the tick count is not an accurate statistic to represent execution time, especially in this case, as the sorting does add an overhead not captured.

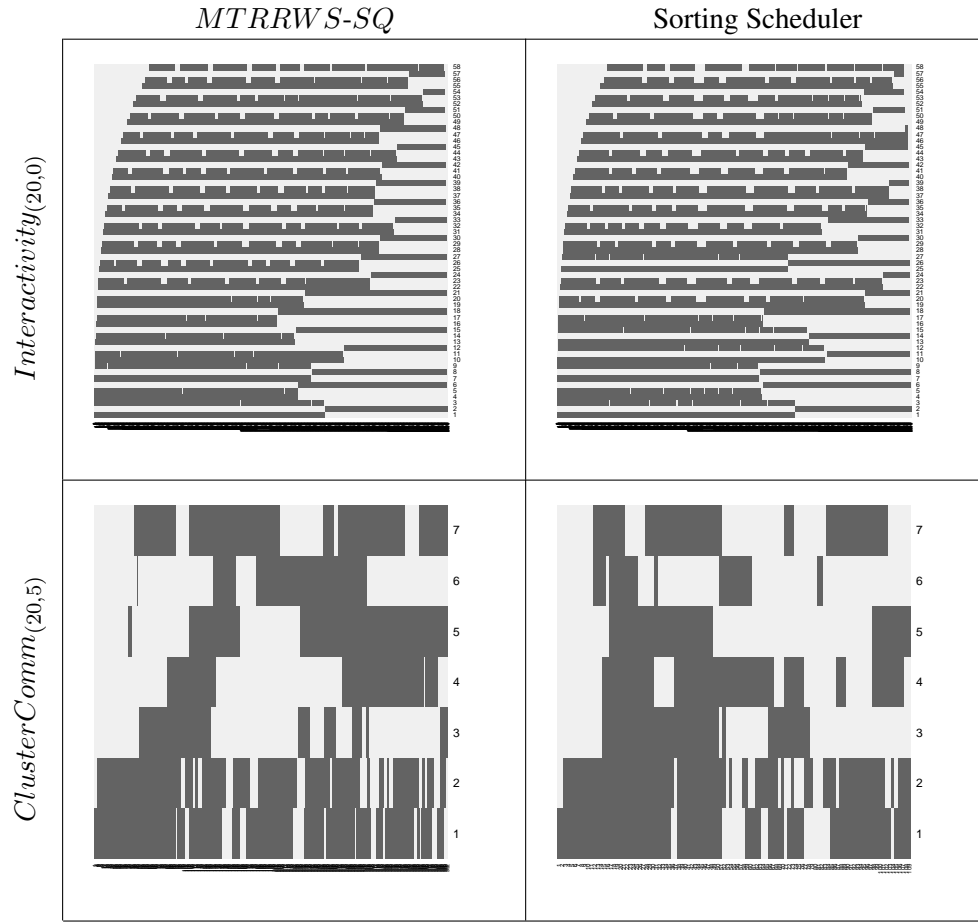


Table 4.9: Channel State comparisons of Random and Uniform synchronization for $MTRRWS-SQ$ and the Bipartite-Graph Aided Sorting Scheduler using Absorption Channels.

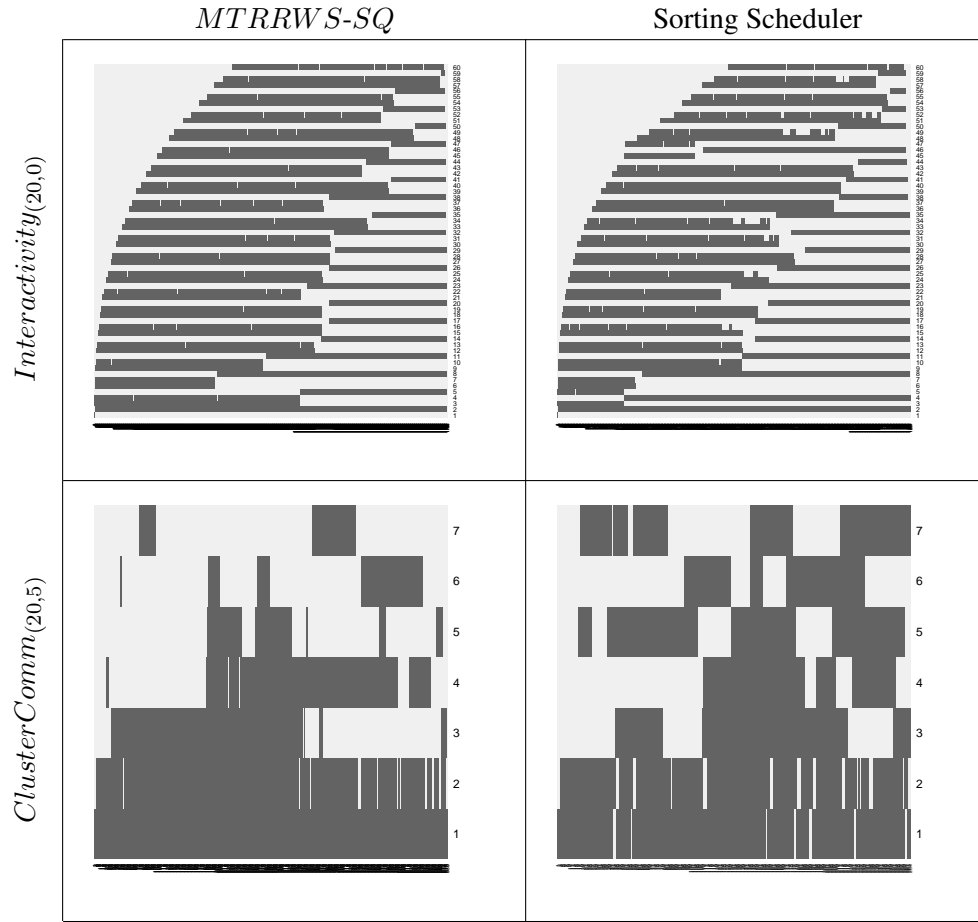


Table 4.10: Channel State comparisons of Random and Uniform synchronization for *MTRRWS-SQ* and the Bipartite-Graph Aided Sorting Scheduler on Blocking Channels.

While, absorption channels can remove chances the scheduler may have of improving the runtime quality of the application. Blocking channels, on the other hand, can leak hints to the scheduler, which it may then use to predict the future. Table 4.10 displays a re-execution of the above tests utilizing Blocking channels instead.

While these charts seem fairly similar there are two things of note. Random communication does not lead to much improvement, despite the apparent difference in execution time. Whereas, if there is a uniform set of processes which communicate frequently, the

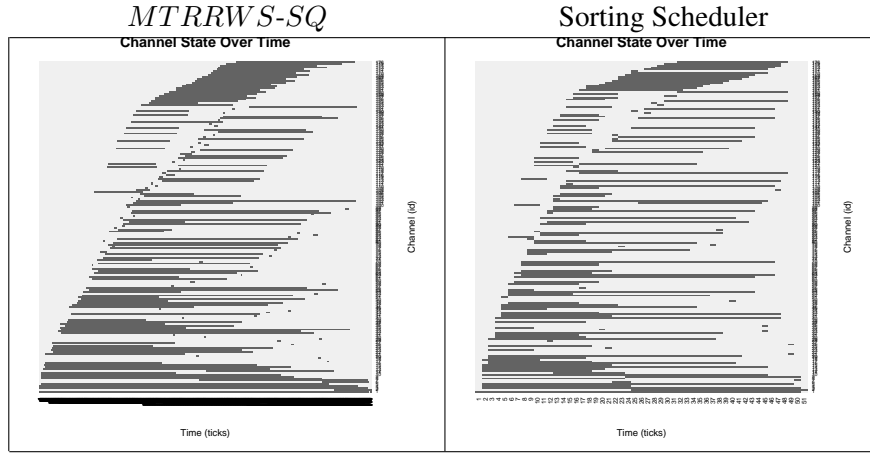


Table 4.11: Channel State comparison of Parallel Fibonacci executed on *MTRRWS-SQ* and the Bipartite-Graph Aided Sorting Scheduler.

sorting algorithm can place them next to one another in the process queue to quickly unblock the channel. This result is consistent with our hypothesis. However, it is much more apparent in the following section.

4.2.3.2 Optimal-Case Scenario

We now compare the execution of the Parallel Fibonacci application with the Sorting scheduler and *MTRRWS-SQ*. To do so, we look at the Channel State graphs to examine the lengths of time channels are blocked. A positive result would be one where the Sorting scheduler visually reduces the length of time channels are blocked on average. Table 4.11 presents this comparison. We attempted to find the 10th Fibonacci number, using the algorithm presented in figure 4.2 using the default sort frequency and algorithm.

We mention now, as we did in section 4.1.2, the effects of scaling in the graph. The number of ticks visualized in the Sorting scheduler's graph is exactly 51, whereas in the other it is over 1000. The fact that they look identical at this scale is a testament to the fact that the reordering of process queues is an effective means by which to speed up execution.

Ultimately this prompts further discussion on the channel implementation.

4.3 A Comment on Swap Channels

Swap channels provided a number of benefits on the side of the language designer. They reduce the complexity of channel implementation, and as shown, they lend themselves to a number of possible designs. We demonstrated just two possible implementations, the Blocking and Absorption channels. As such, the concept of a swap channel as a language primitive is extremely attractive. However, swapping poses some problems for realistic applications which we would now like to discuss.

First, a swap channel does not lend itself to a level of fairness that would be expected by a programmer. We lead with our implementation of $ClusterComm_{(N,M)}$ as example, which due to being on top of swap channels, was made to be much more enigmatic. Figure 4.4 gives an example implementation of $ClusterComm_{(N,M)}$. Note the third parameter to the application, which denotes the number of time's each of the N processes should communicate. The system therefore blocks until all processes synchronize X times before quitting.

Due to this, we must pose several restrictions on the possible values of N and M . Namely, N must be an even number, since all processes would need to have a partner to swap with, and M must be no greater than $\lfloor N/2 \rfloor$, any more and it would be possible for a process to hang indefinitely.

However, these two restrictions are not enough to guarantee the process terminates. In fact, most runs of this application, with any value of $N > 2$ would most likely hang forever. This is due to the bias our program creates when spawning processes, as well as the type of fairness the swap channel semantics provides.

First, our bias we introduce is merely because we cannot batch spawn a set of

```

fun N,M,X. // Number of Processes, Channels, and times a Process should Swap
(
  // Create the list of channels for random access.
  let chanlist = (omega fun f,L,s.(if (leq s 0)
                                     L
                                     (f f (cons newchan L) (dec s)))
                newlist M)
  in
    // Randomly swap, by selecting random channel in chanlist.
    let rswap = fun _.(swap (index chanlist (rand M)) nil)
    in
      // Worker Thread, loops for X times and randomly swaps.
      let worker_t = fun _.(omega fun f,c.(if (leq c 0)
                                              1
                                              (ignore (rswap 0)
                                                         (f f (dec c)))))
      X)
    in
      // "Main" Function, spawns N worker threads and waits for their return.
      (omega fun f,c.(if (leq c 1)
                        (worker_t nil)
                        (merge (fun _.(f f (dec c))
                                worker_t
                                ignore))
                        N)
      )
)

```

Figure 4.4: A naive but ineffectual $ClusterComm_{(N,M)}$ implementation.

processes at the same time. As such we will spawn one process at a time which may get a chance to run before all others. As such the first several processes may reach their synchronization limit before we are even done spawning the rest of the processes. Due to this, we may have a case where all but M processes have completed, and thus all channels are blocked indefinitely.

Secondly, the channel semantics have no inherent preference for unseen or new processes. The scheduler may easily get in a loop of running the same subset of processes repeatedly, this would have the same effect as the above even if we were able to solve the bias problem. As such, this is inherently an issue with the capabilities of swap channels.

Thus, the best we can do for $ClusterComm_{(N,M)}$, is to run until at least $N - M$ processes have met their quota. Note this approach is only acceptable under Symmetric message passing constructs. In asymmetrical, even if there was a guarantee of an equal number of senders and receivers, all senders could be blocked on one channel while all receivers could be blocked on another.

But this issue points to another problem with swap channels, insofar as they do not lend themselves to being primitives at all. Due to this fairness issue, a language with swap channels would be unable to build the asymmetrical constructs most user's would like. As such, they have been useful merely for simulation purposes.

However, for further simulation of cooperativity, it may be advantageous to also consider the directionality of communication. The recognition of consumer and producer processes may lend itself to further gains as the recognition of communication and computation bound processes did. This is not to say all gains in utilizing pure synchronization have been obtained.

Chapter 5

Conclusion and Future Work

5.1 Utility of the ErLam Toolkit

To test our scheduling mechanisms, the ErLam Toolkit provided a powerful framework for their implementation and execution observation. The testing primitives provided provide a concise way to reference common behavior and subsequently compare executions. The logging system was also precise enough to allow for further statistical comparison between multiple runs of the same test case. This allowed for easier fine-tuning as well as general algorithm debugging.

5.2 Effectiveness of Cooperativity as a Metric

Our original goal was to show several potentially advantageous mechanisms schedulers could use to take advantage of process cooperativity. We believe that we were able to validate most of our hypotheses and show that cooperativity can be a powerful metric by which to govern a scheduler. With analysis of multiple test cases including boundary and common conditions, we proved the practicality of these mechanisms as well. Ultimately we were able to open more avenues of possible research in this field that we hope to explore further.

5.3 Future Work

There are a number of appealing avenues of improvement for the ErLam Toolkit. The report generation mechanism could be extended and tied into the logging system a bit

more closely. For example, the implementation of a real-time viewer would be an interesting extension. Also, there are obviously a larger number of metrics which may lead to better cooperativity classifications as well. It may be more fruitful, for example, to keep track of communication partners rather than channel names. The core language is also an appealing simulation implementation language, as such, a larger library of testing primitives would benefit the language designer community greatly. Furthermore, a complete catalog of parameterized executions would also aid in analysis and scheduler comparison.

In terms of cooperativity as a feedback metric, it would be interesting to further tweak the three cooperativity-conscious schedulers already implemented. Perhaps composing the scheduler mechanics themselves may lead to a more robust algorithm. For example, the combination of the Batching and Channel Pinning may lead to a better work stealing mechanism as batches now belong to channels. Alternatively the ability to sort batches might speed up the sorting process. It would also reduce the search space for finding related processes when it came time to resort. However, in short, the future of cooperativity as a feedback metric looks promising.

Appendix

Appendix 1

ErLam Test Cases

We provide here, for the reader's analysis, the source code of the ErLam test cases we utilized throughout the testing and evaluation phases of our document.

```
//  
// ChugMachine: Parallel non-cooperative processes  
//  
fun N, // Number of Processes  
    A, // Minimum number of reductions per worker.  
    B. // Max number of reductions per worker.  
(  
    // WORKER:  
    let worker_t = fun _.(chug (add A (rand (sub B A)))) in  
  
    // MANAGER:  
    (omega fun f,c.(if (leq c 1)  
                      (worker_t nil)  
                      (merge (fun _.(f f (dec c)))  
                              worker_t ignore))  
      N)  
)
```

Figure 1.1: ChugMachine application, generates N processes to just compute.

```

//
// UserInput: Single Event processing simulation
//
fun X, // Number of events to consume before quitting.
    H, // Max number of seconds to wait for an event.
    L. // Min number of seconds to wait for an event.
(
    // Event Channel
    let chan = newchan in

    // User
    let get_event = fun c.(ignore (hang (add L (rand (sub H L)))))
                                (swap chan c)) in
    let user_t = fun start,_.(omega fun f,c.(
                                if (leq c 0) (swap chan nil)
                                    (ignore (get_event c)
                                        (f f (dec c))))
                                start)

    in
    let _ = (spawn (user_t X)) in

    // Consumer/MAIN
    (omega fun f.(if (swap chan nil) (f f) 1))
)

```

Figure 1.2: UserInput application, utilized in the Interactivity composure, it artificially hangs without anything to do for a random period of time so as to simulate an interactive process.

```

//
// ClusterComm: Parallel Full-App Communication
// Simulates unstructured communication across all processes in the
//
fun N, // Number of Processes in the system.
    M, // Number of channels for synchronization.
    X. // Number of times each process should synch.
(
    // Result Notification:
    let win_chan = newchan in
    let result_chan = newchan in
    let wait_for = (sub N M) in
    let _ = (spawn fun _.(omega fun f.(ignore (swap win_chan result_chan)
                                                (f f)))) in

    // Channel List setup:
    let chanlist = (omega fun f,l,s.(if (leq s 0) 1
                                         (f f (cons newchan l) (dec s)))
                   newlist M)

    in
    let rswap = fun _.(swap (index chanlist (rand M)) nil) in

    // WORKER:
    let rchug = fun n.(chug (rand n)) in
    let finish = fun _.(omega fun f.(let r = (swap win_chan nil)
                                         in (if (is_chan r)
                                                (swap r 1)
                                                (ignore (rchug 5) (f f)))) in

    let worker_t = fun _.(omega fun f,c.(if (leq c 0) (finish nil)
                                             (ignore (rswap 0) (f f (dec c))))) X)

    in

    // MAIN FUNCTION:
    let _ = // Spawn Worker Set
            (spawn fun _.(omega fun f,c.(if (leq c 1)
                                             (worker_t nil)
                                             (ignore (spawn worker_t)
                                                         (f f (dec c)))))
              N))

    in // Hang for Result
        (omega fun f,c.(if (leq c 0) 1
                           (ignore (swap result_chan nil) (f f (dec c)))))
        wait_for)
)

```

Figure 1.3: ClusterComm application, generates N processes and M channels, and waits for at least $N - M$ processes to finish synchronizing X times.

```

//
// PRing: Simulate full-system cooperativity via a ring network
//
fun N, // Ring size in number of processes.
    L, // Number of times to pass the token around the ring.
    C. // Number of times to chug at each loop around the ring.
(
    let token = 1 in
    let kill_token = 2

    // An individual node in the ring.
    in let node = fun rside, lside, _ . (omega fun f . (
        let rcv = (swap rside nil) in
        let _ = (chug C) in
        let dead = (eq kill_token rcv) in
        let _ = (swap lside rcv) in
        if dead nil //(println 1)
        (f f)))

    // Parent node which will count the loops.
    in let pnode = fun rside, lside . (omega fun f, c . (
        let rcv = (swap rside nil) in
        let dead = (eq c L) in
        let _ = (swap lside (if dead kill_token rcv)) in
        if dead nil //(println 1)
        (f f (inc c))))

    // Start the Parent node after starting the token passing.
    in let start_pnode = fun rside, lside . (
        let _ = (swap lside token) in
        (pnode rside lside 1))

    // Spawns a new node with a channel and returns it's left side channel.
    in let spawnnode = fun rside . (
        let lside = newchan in
        (ignore (spawn (node rside lside)) lside))

    // Generate the ring and connects the last two. Then triggers the
    // ring to start passing.
    in let mychan = newchan
    in let last_chan = (rep N spawnnode mychan)
    in (start_pnode last_chan mychan)
)

```

Figure 1.4: PRing application, simulates full-system cooperativity, where N processes pass a token around a ring network.

```

//
// P-TREE: Parallel Cooperative Sets
// Spawns W work-set and M cooperative processes per set. The processes
// then run a random number of reductions and then proceed to synchronize.
// They do this X times before stopping.
//
fun W, // Number of Work-Sets
    M, // Number of Members per Work-Set
    X. // Number of times members should synch
(
    let clustercomm = (
    {{{ClusterComm.els}}}
    ) in

    // A workgroup thread of M processes and X syncs
    let workgroup_t = (fun _.(clustercomm M 1 X)) in

    // Run W workgroups
    (omega fun f,w.(if (leq w 1)
                        (workgroup_t nil)
                        (merge (fun _.(f f (dec w)))
                              workgroup_t
                              ignore))
      W)
)

```

Figure 1.5: PTree application, utilizes composure of ClusterComm to simulate partial-system cooperativity as a set of work-groups.

```

//
// Interactivity Testing
//
fun X, // Number of UserInput processes to spawn.
    Y. // Number of processes to spawn in the ChugMachine
(
    // IMPORTED FUNCTION DEFINITIONS
    let userInput = (
{{{UserInput.els}}}
    ) in
    let chugmachine = (
{{{ChugMachine.els}}}
    ) in

    // User Input thread and generation loop.
    let ui_t = fun _.(userinput 10 5 1) in
    let ui_l = (omega fun f,c.(if (leq c 1)
                                (ui_t nil)
                                (merge ui_t
                                     fun _.(f f (dec c))
                                     ignore)))

    // Spawn X User inputs and Y computational-bound processes
    // - Wait for all to finish before returning.
    in (merge (fun _.(chugmachine Y 200 150))
            (fun _.(ui_l X))
            ignore)
)

```

Figure 1.6: Interactivity Application, utilizes a composure of UserInput.els and ChugMachine.els to test a scheduler’s ability to handle interactivity.

```

//
// JumpShip: Parallel Workgroups which hop channels
//
fun N,M,C, // Number of Processes, Channels and Chugs.
    T,X, // Number of Rounds and iterations per Round.
(
    // List Access
    let chanlist = (omega fun f,l,s.(if (leq s 0)
                                      1
                                      (f f (cons newchan l) (dec s)))
                    newlist M) in
    let next_chan_index = fun i.(if (eq i M) 1 (inc i)) in

    // WORKER:
    let worker = fun cl,init,_.(
        let run_round = (fun c.(ignore (chug C) (swap c nil))) in
        (omega fun f,r,i,c,n.(
            if (leq r 0) 1
            (if (gt i 1)
                (ignore (run_round c) (f f r (dec i) c n))
                (let new_n = (next_chan_index n) in
                 let new_c = (index cl new_n) in
                 (f f (dec r) X new_c new_n))))
        T X (nth cl init) init))
    in

    // MANAGER:
    let worker_t = (worker chanlist) in
    let workers_per_chan = (div N M) in
    let manager_t = fun ci,_.(omega fun f,c.(
        if (leq c 1) (worker_t ci nil)
        (merge (fun _.(f f (dec c)))
                (worker_t ci)
                ignore))
        workers_per_chan)
    in

    // MAIN Function: - Spawn M managers for each initial channel group.
    // The groups all start at their respective index in the channel list (i.e.
    // group 1 is index 1, group 2 is index 2, etc.) On each round their channel
    // index is incremented and modulo M.
    (omega fun f,c.(
        if (leq c 1) (manager_t c nil)
        (merge (fun _.(f f (dec c)))
                (manager_t c)
                ignore)) M)
)

```

Figure 1.7: JumpShip application, similar to PTree and ClusterComm, except posesses to demonstrate application phases as the work-groups change channels.

Bibliography

- [1] Kurt Debattista, Kevin Vella, and Joseph Cordina. "Cache-affinity scheduling for fine grain multithreading." In: *Communicating Process Architectures 2002* (2002), pp. 135–146.
- [2] Robin Milner. *A calculus of communicating systems*. Springer-Verlag New York, Inc., 1982.
- [3] Catuscia Palamidessi. "Comparing the expressive power of the synchronous and the asynchronous Π -calculus." In: *Proceedings of the 24th ACM SIGPLAN-SIGACT symposium on Principles of programming languages*. ACM. 1997, pp. 256–265.
- [4] Evangelos P Markatos and Thomas J LeBlanc. "Load balancing vs. locality management in shared-memory multiprocessors." In: (1991).
- [5] Evangelos P Markatos and Thomas J LeBlanc. "Memory-Conscious Scheduling in Shared-Memory Multiprocessors." In: *Computer Science Dept., University of Rochester* (1991).
- [6] Bashir Alam, MN Doja, and R Biswas. "Finding time quantum of round robin cpu scheduling algorithm using fuzzy logic." In: *Computer and Electrical Engineering, 2008. ICCEE 2008. International Conference on*. IEEE. 2008, pp. 795–798.
- [7] Samih M Mostafa, SZ Rida, and Safwat H Hamad. "Finding time quantum of round robin CPU scheduling algorithm in general computing systems using integer programming." In: *International Journal of Research and Reviews in Applied Sciences (IJRRAS)* 5.1 (2010), pp. 64–71.
- [8] Shahram Saeidi and Hakimeh Alemi Baktash. "Determining the Optimum Time Quantum Value in Round Robin Process Scheduling Method." In: *International Journal of Information Technology and Computer Science (IJITCS)* 4.10 (2012), p. 67.
- [9] Michael R Garey, Ronald L Graham, and DS Johnson. "Performance guarantees for scheduling algorithms." In: *Operations Research* 26.1 (1978), pp. 3–21.
- [10] Richard D Dietz et al. "The use of feedback in scheduling parallel computations." In: *Parallel Algorithms/Architecture Synthesis, 1997. Proceedings., Second Aizu International Symposium*. IEEE. 1997, pp. 124–132.
- [11] Wikimedia Commons. *Feedback loop with descriptions*. Accessed 8/2014. URL: https://commons.wikimedia.org/wiki/File:Feedback_loop_with_descriptions.svg.
- [12] Fernando J Corbató, Marjorie Merwin-Daggett, and Robert C Daley. "An experimental time-sharing system." In: *Proceedings of the May 1-3, 1962, spring joint computer conference*. ACM. 1962, pp. 335–344.

- [13] Remzi H. Arpaci-Dusseau and Andrea C. Arpaci-Dusseau. *Operating Systems: Three Easy Pieces*. 0.80. Arpaci-Dusseau Books, 2014.
- [14] Kenneth Hoganson. “Reducing MLFQ scheduling starvation with feedback and exponential averaging.” In: *Journal of Computing Sciences in Colleges* 25.2 (2009), pp. 196–202.
- [15] John H Reppy. “Concurrent ML: Design, application and semantics.” In: *Functional Programming, Concurrency, Simulation and Automated Reasoning*. Springer. 1993, pp. 165–198.
- [16] Carl G Ritson, Adam T Sampson, and Frederick RM Barnes. “Multicore scheduling for lightweight communicating processes.” In: *Science of Computer Programming* 77.6 (2012), pp. 727–740.
- [17] Don Jones Jr, Simon Marlow, and Satnam Singh. “Parallel performance tuning for Haskell.” In: *Proceedings of the 2nd ACM SIGPLAN symposium on Haskell*. ACM. 2009, pp. 81–92.
- [18] David R White et al. “Automated heap sizing in the poly/ML runtime.” In: *Trends in Functional Programming* (2012).
- [19] Kunal Agrawal et al. “Adaptive work-stealing with parallelism feedback.” In: *ACM Transactions on Computer Systems (TOCS)* 26.3 (2008), p. 7.
- [20] Yuxiong He, Wen-Jing Hsu, and Charles E Leiserson. “Provably efficient online non-clairvoyant adaptive scheduling.” In: *Parallel and Distributed Systems, IEEE Transactions on* 19.9 (2008), pp. 1263–1279.
- [21] John L Bruno et al. *Computer and job-shop scheduling theory*. Wiley, 1976.
- [22] Thomas L. Casavant and Jon G. Kuhl. “A taxonomy of scheduling in general-purpose distributed computing systems.” In: *Software Engineering, IEEE Transactions on* 14.2 (1988), pp. 141–154.
- [23] Hagai Abeliovich. “An empirical extremum principle for the hill coefficient in ligand-protein interactions showing negative cooperativity.” In: *Biophysical journal* 89.1 (2005), pp. 76–79.