

Atomic Layer Deposition of Ferroelectric HfO₂

Casey Gonta

R·I·T

KATE GLEASON
College of ENGINEERING



Outline

- **Introduction**
 - Background
 - Previous Work at RIT
 - Project Objectives

- **Process**
 - ALD Development
 - Fabrication of Capacitor Devices

- **Results**
 - TEM and EELS
 - CV Measurements

- **Conclusions and Future Work**



Outline

- **Introduction**
 - Background
 - Previous Work at RIT
 - Project Objectives

- **Process**
 - ALD Development
 - Fabrication of Capacitor Devices

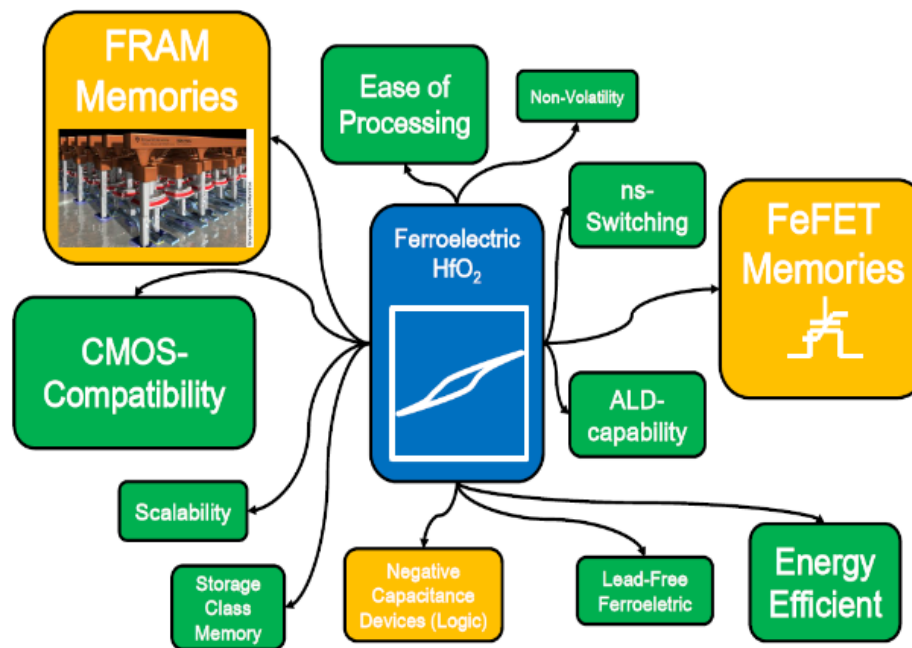
- **Results**
 - TEM and EELS
 - CV Measurements

- **Conclusions and Future Work**



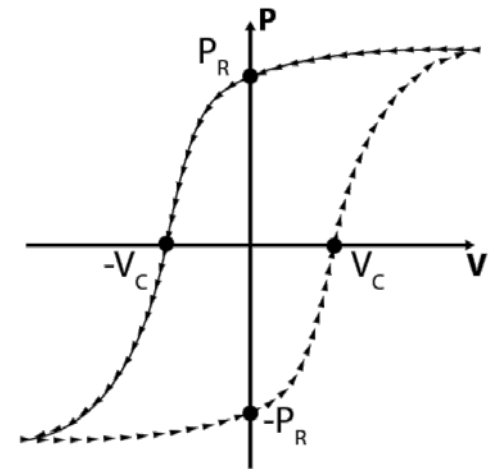
Background

- Hafnium oxide (HfO₂) is a high permittivity dielectric material (dielectric constant $k \sim 25$)
- HfO₂ has replaced SiO₂ as the gate dielectric in silicon CMOS since the 45nm node
- Doped HfO₂ has shown to be ferroelectric in nature expanding its applications to devices such as FeFETs, FRAM, NC FETs, and FTJs



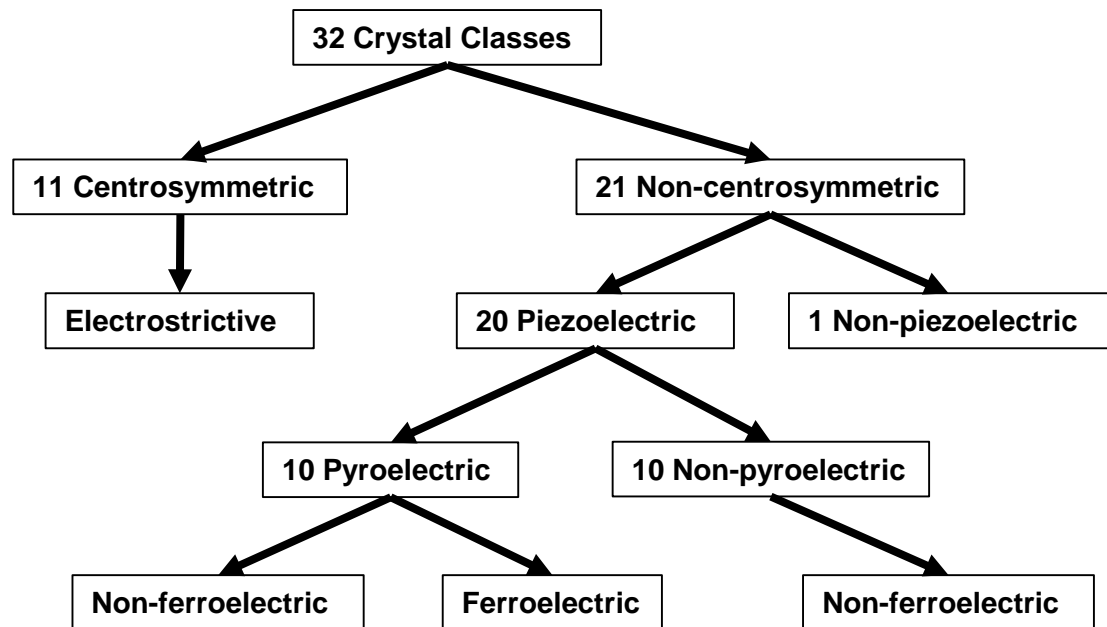
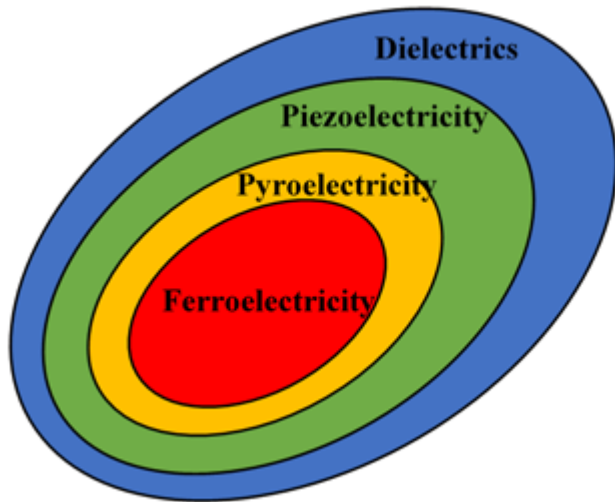
What is Ferroelectricity?

- Ferroelectric (FE) materials exhibit spontaneous polarization making them particularly attractive for non-volatile memory and logic applications
- In ferroelectric material when no bias is applied, Remnant Polarization (P_R) is present
- With no polarization, a Coercive Field (V_C) is present



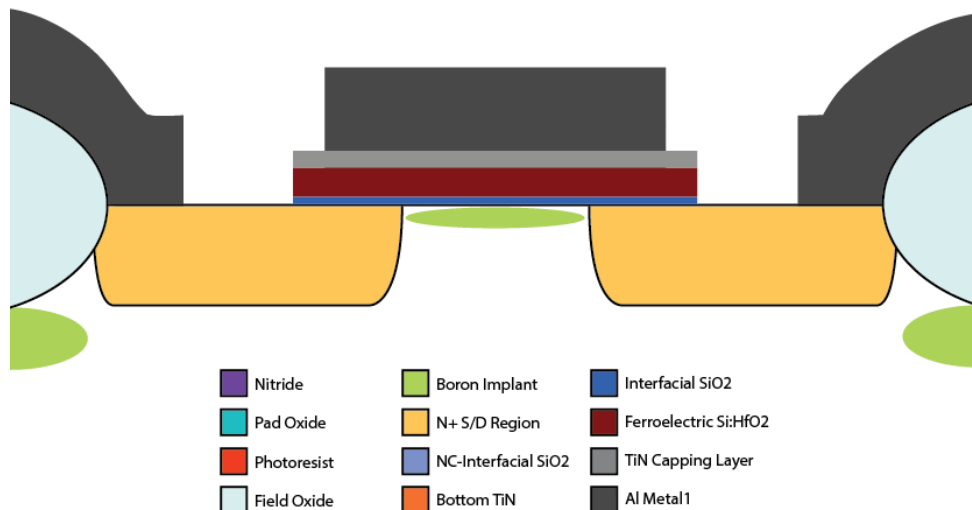
What is Ferroelectricity?

- Within the realm of Dielectrics exist:

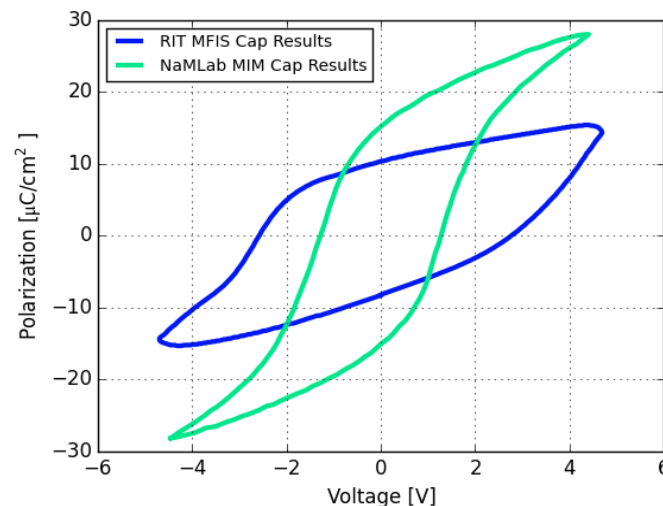


Previous Work at RIT

- Ferroelectric transistors (FeFETs) have been fabricated at RIT by Joe McGlone
- Collaboration and ferroelectric Si:HfO₂ depositions from NaMLaB in Dresden, Germany



Comparing NaMLab and RIT Capacitor P-V Curves



Project Objectives

Goal: To engineer ferroelectric HfO₂ solely at RIT by:

- **Developing a recipe for atomic layer deposition (ALD) of aluminum doped HfO₂**
- **Fabricating capacitor structures to characterize the performance of the doped and un-doped HfO₂**
- **Conducting polarization and current testing on capacitors to observe and verify ferroelectric properties**

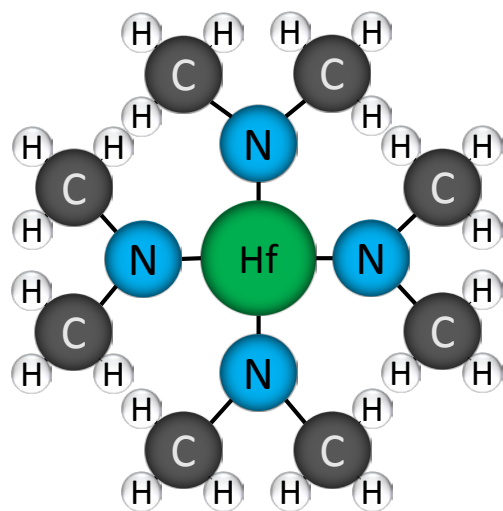


Outline

- Introduction
 - Background
 - Previous Work at RIT
 - Project Objectives
- Process
 - ALD Development
 - Fabrication of Capacitor Devices
- Results
 - TEM and EELS
 - CV Measurements
- Conclusions and Future Work

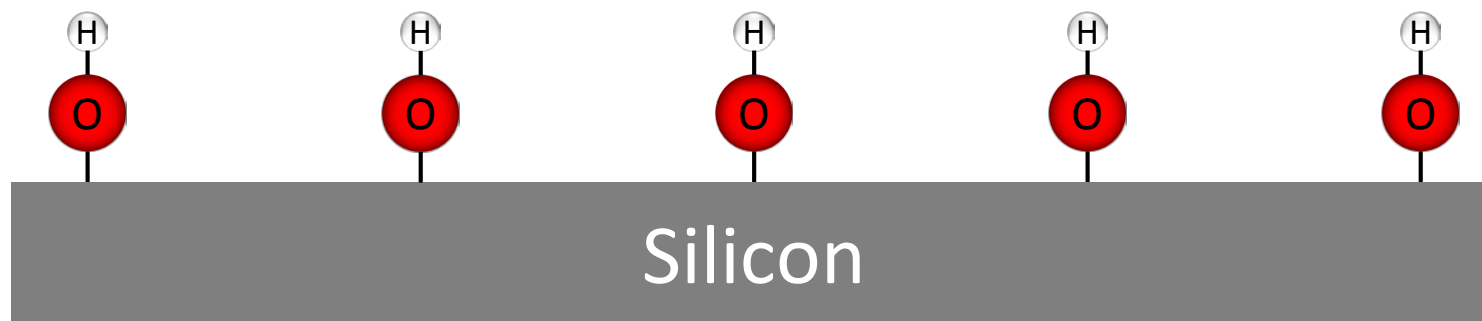


Introduce Hf Precursor to Starting Substrate



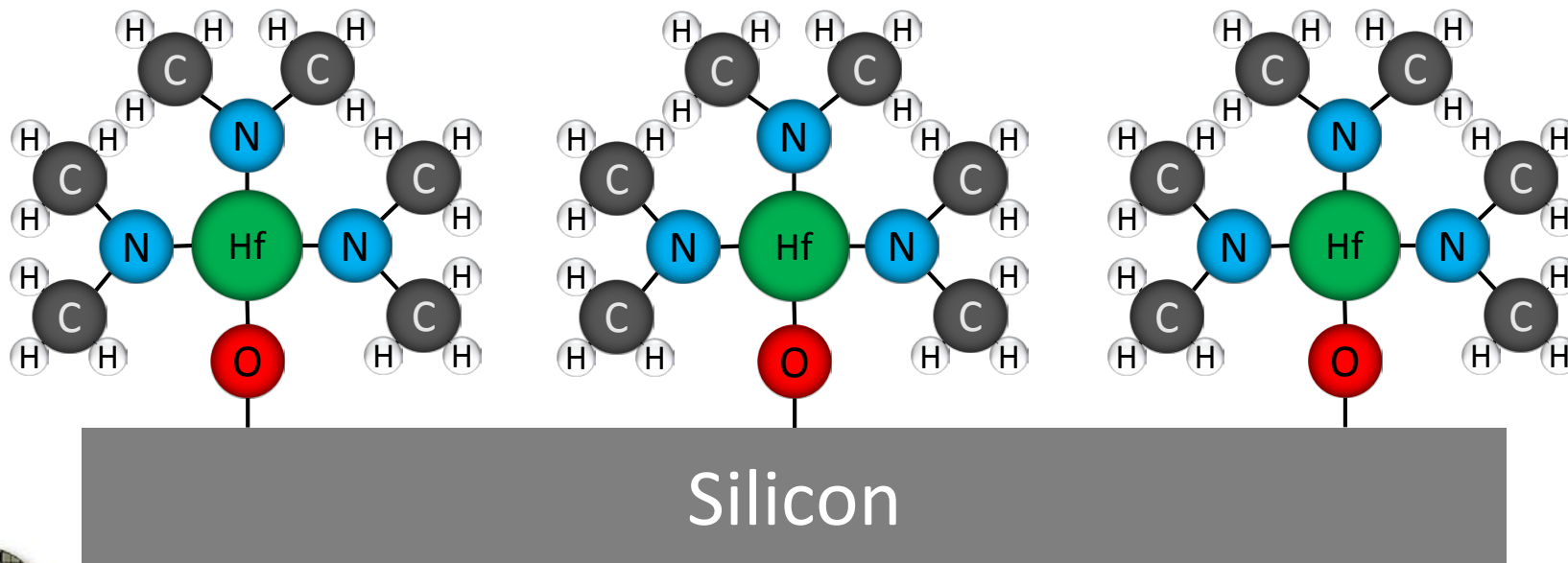
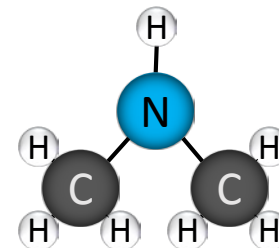
TDMAHf

Tetrakis(dimethylamido)hafnium(IV)



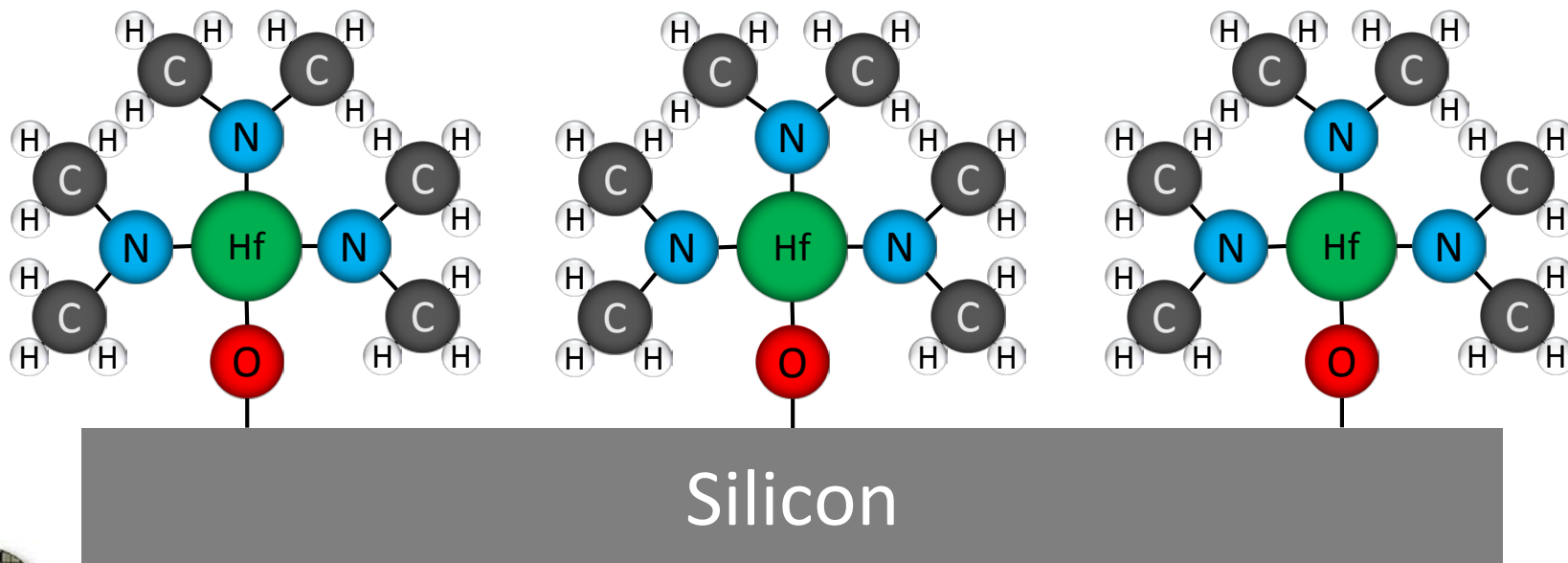
Self Limiting Surface Reactions

- Surface reactions between OH groups and TDMAHf
- By-product is Dimethylamine: (CH₃)₂NH →



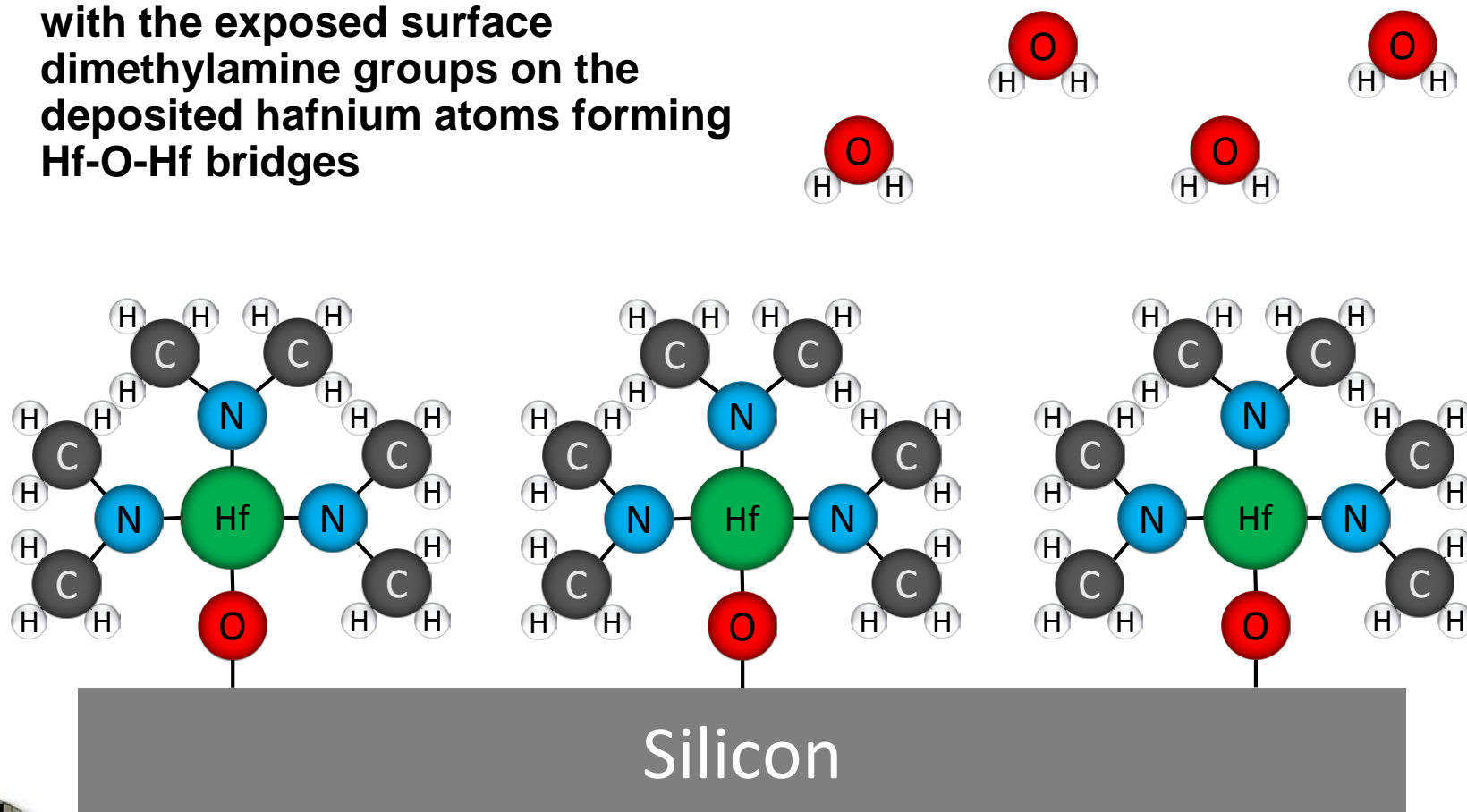
Purge

- The chamber is then purged, removing all unreacted TDMAHf precursor and dimethylamine by-product



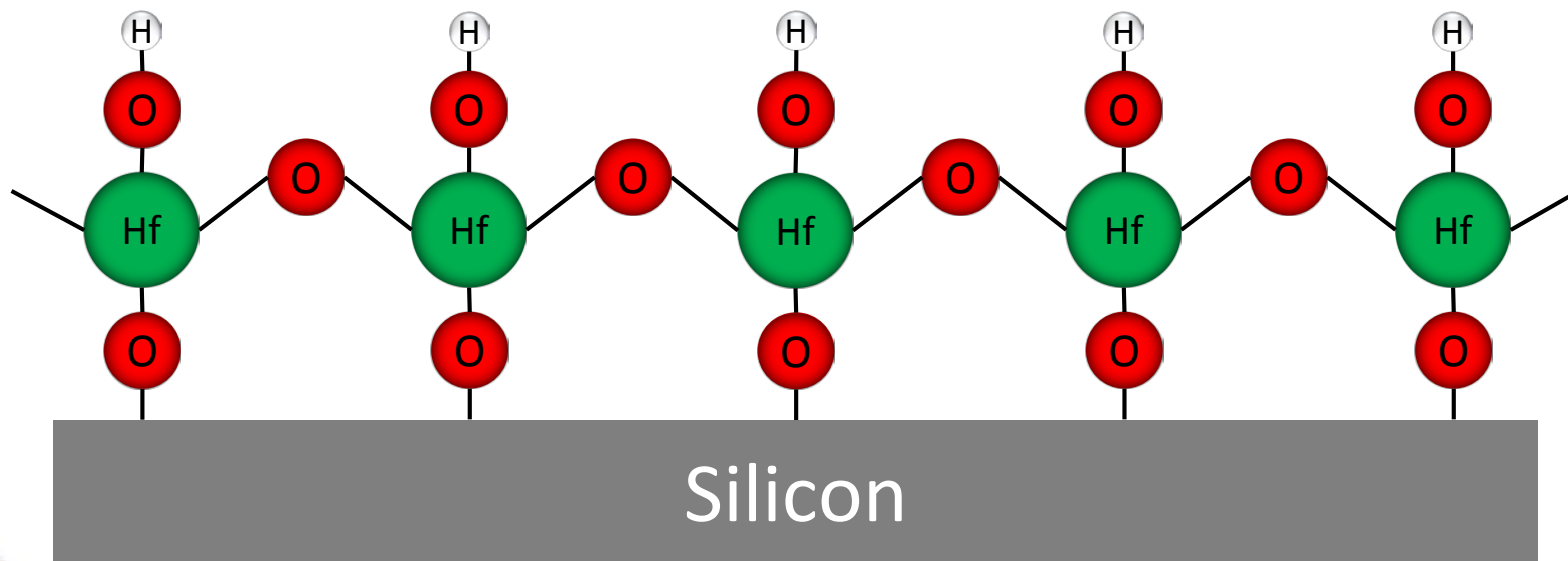
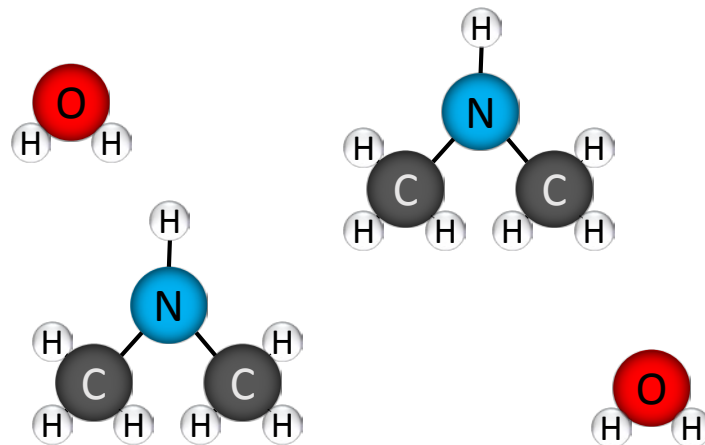
Introduce H₂O

- Water is then introduced to react with the exposed surface dimethylamine groups on the deposited hafnium atoms forming Hf-O-Hf bridges



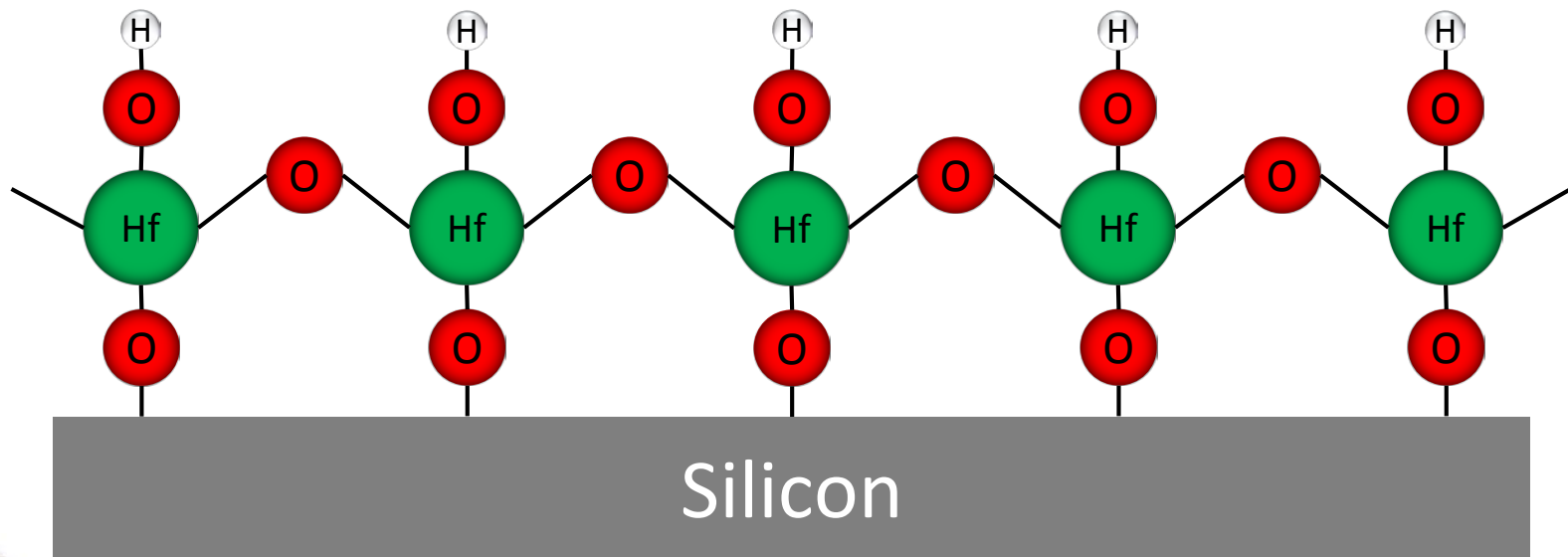
Reaction

- The resulting reaction leaves the surface with hydroxyl groups and Hf-O-Hf bridges
- The chamber then contains unreacted water molecules and the by-product which is once again dimethylamine



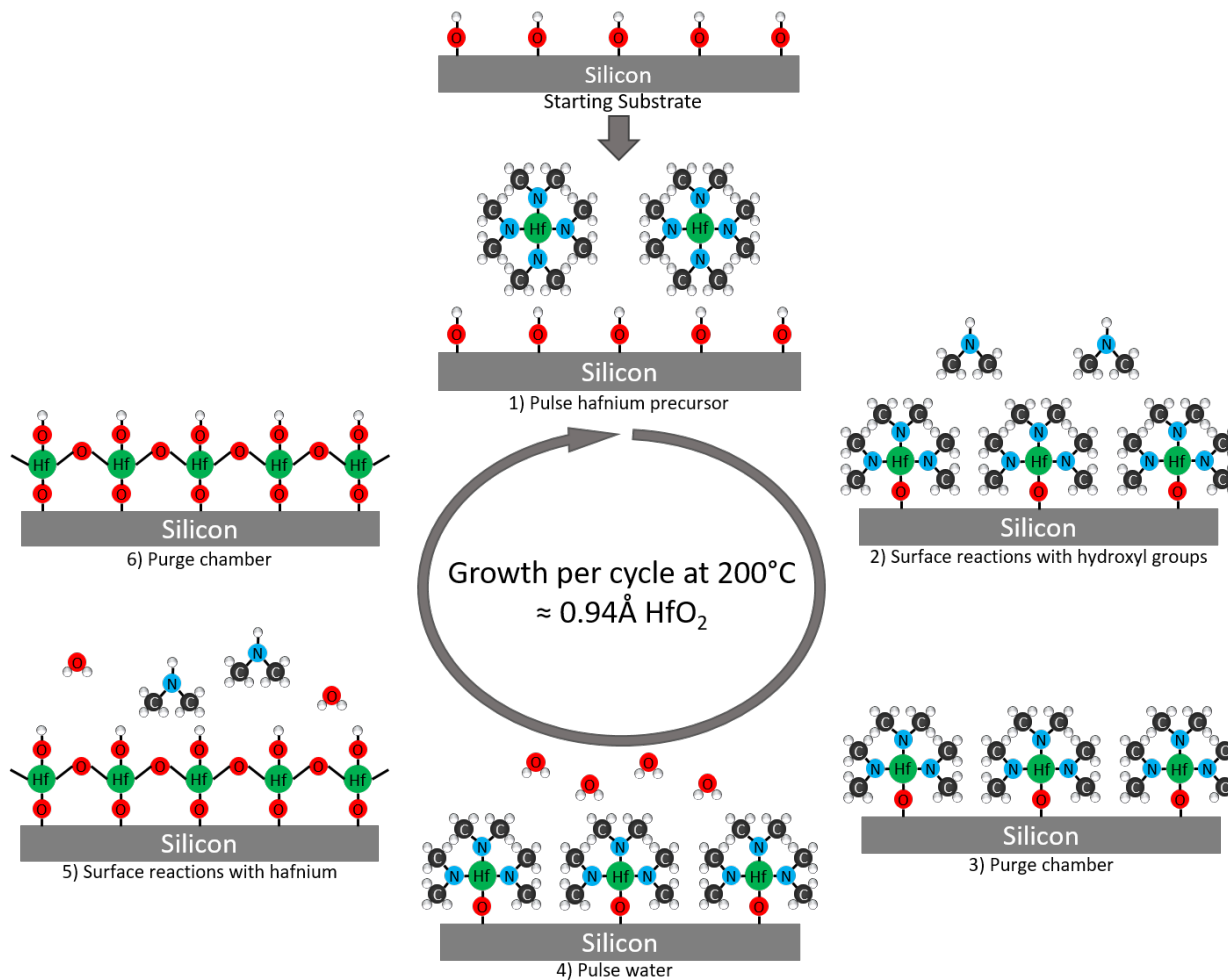
Purge

- Another purge is done to evacuate the chamber of unreacted water molecules and the dimethylamine by-product
- The surface is then ready to accept the next layer of hafnium atoms



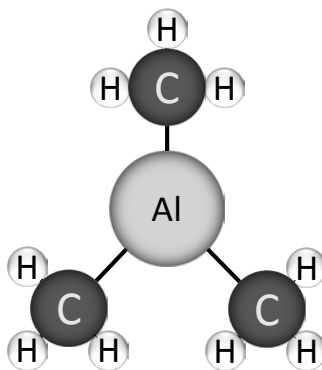
Repeat

- The whole sequence is then repeated to form layer after layer of HfO₂

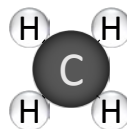


How is the Aluminum Dopant Introduced?

- Once every “X” cycles of hafnium precursor, an aluminum precursor is pulsed and reacts with the surface in the same fashion
- The aluminum precursor used is TMA – trimethyl aluminum: Al(CH₃)₃

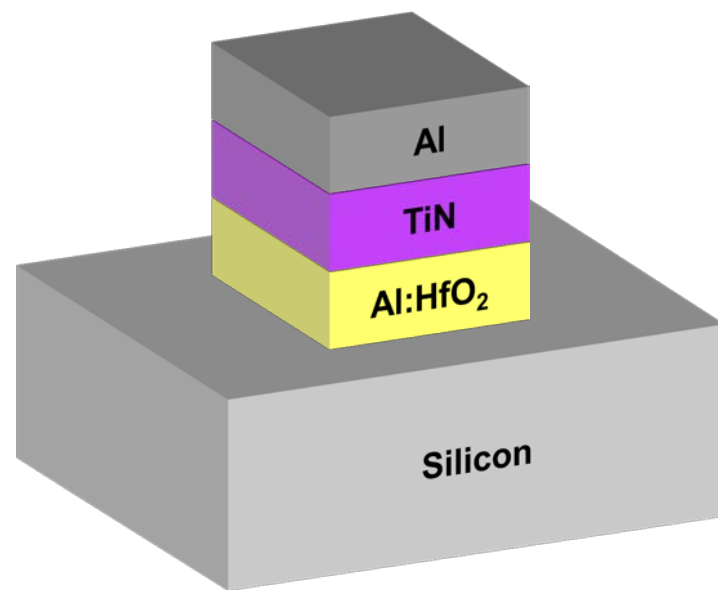
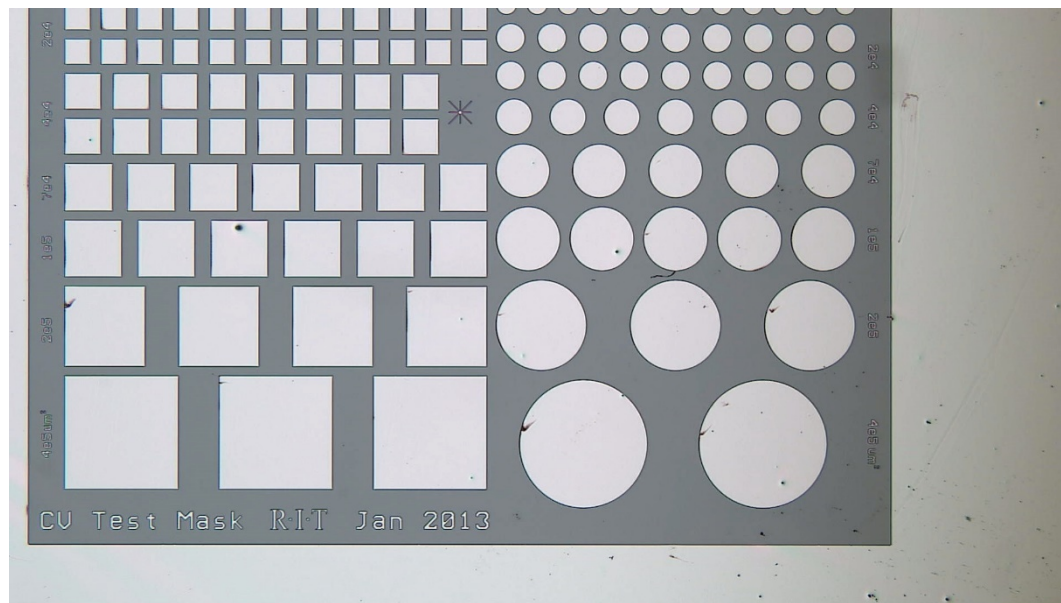


- The by-product of this reaction with the exposed hydroxyl group, as well as the subsequent reaction with water, is simply methane: CH₄



Fabrication of Capacitor Devices

- Capacitor devices were fabricated for electrical testing

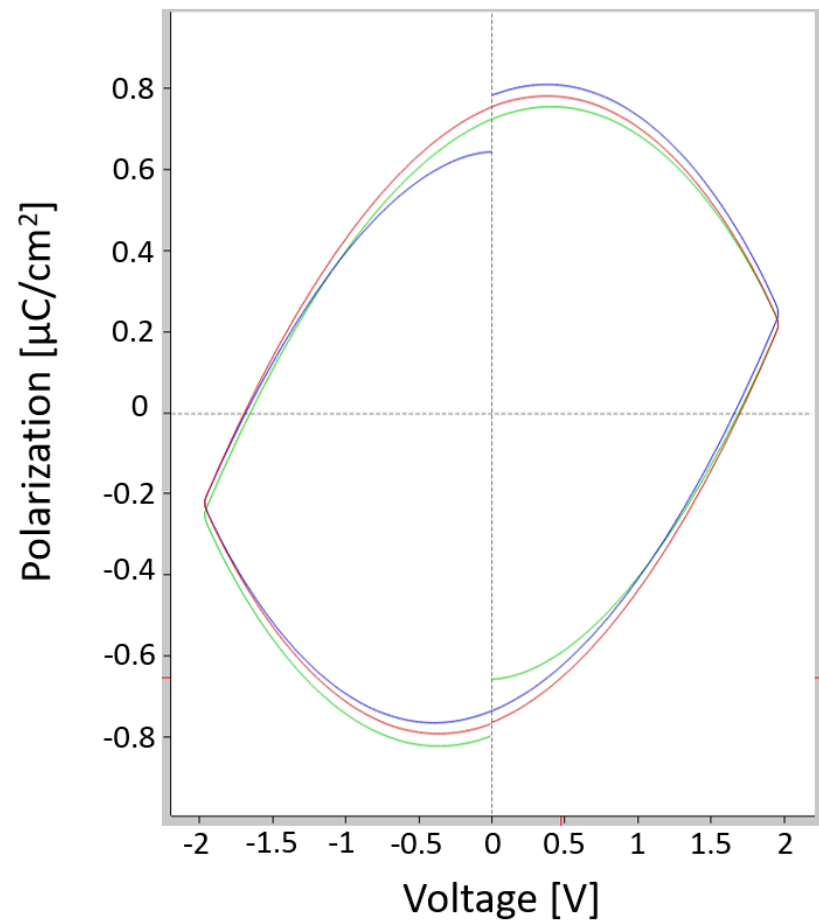
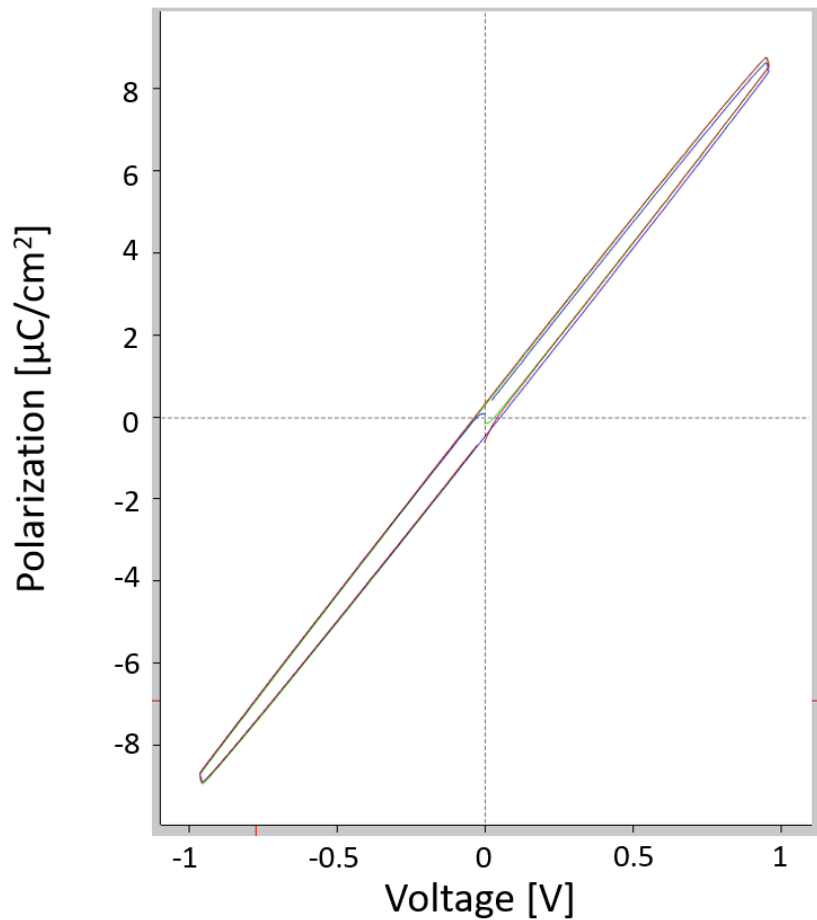


Outline

- **Introduction**
 - Background
 - Previous Work at RIT
 - Project Objectives
- **Process**
 - ALD Development
 - Fabrication of Capacitor Devices
- **Results**
 - TEM and EELS
 - CV Measurements
- **Conclusions and Future Work**

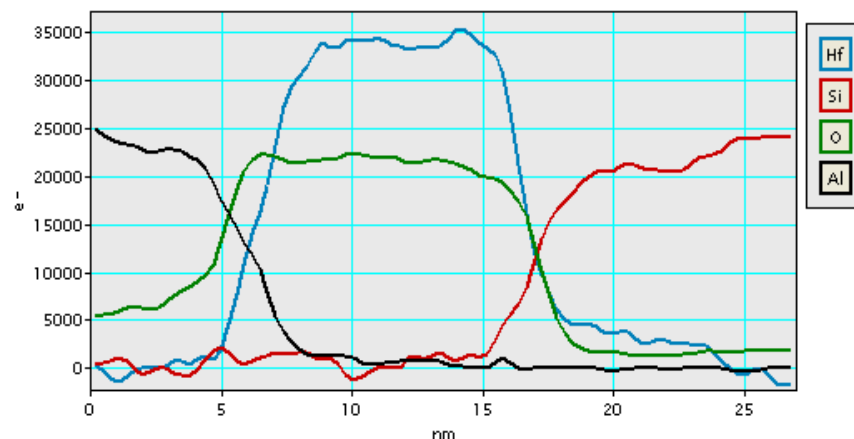
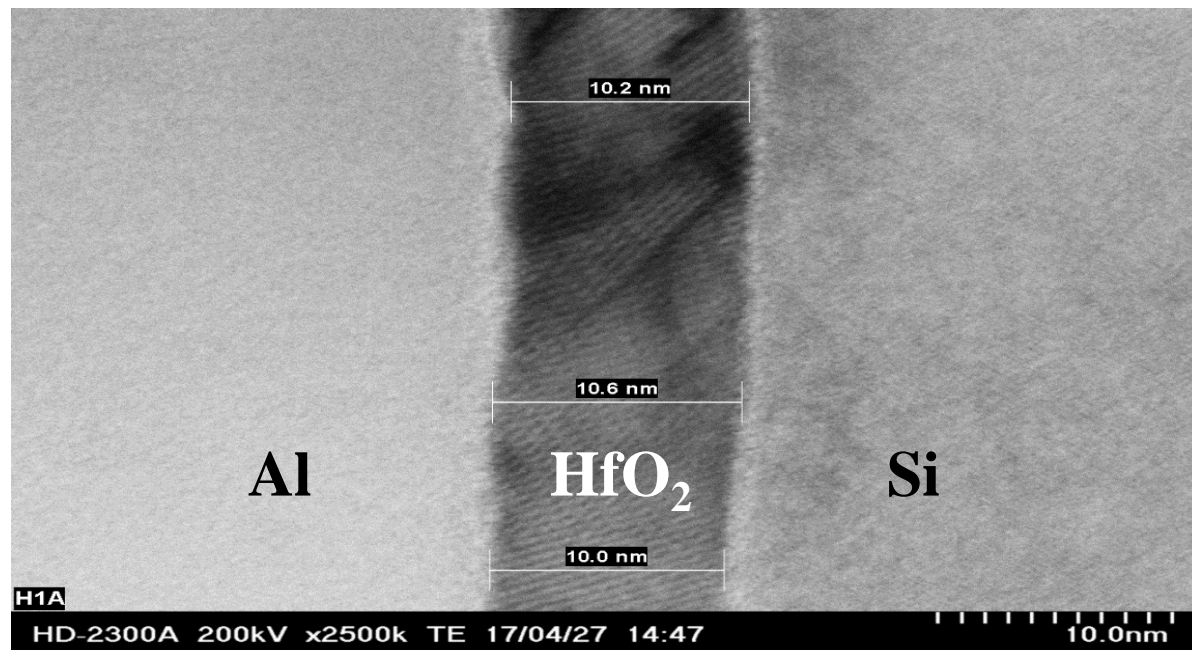


Polarization Measurements

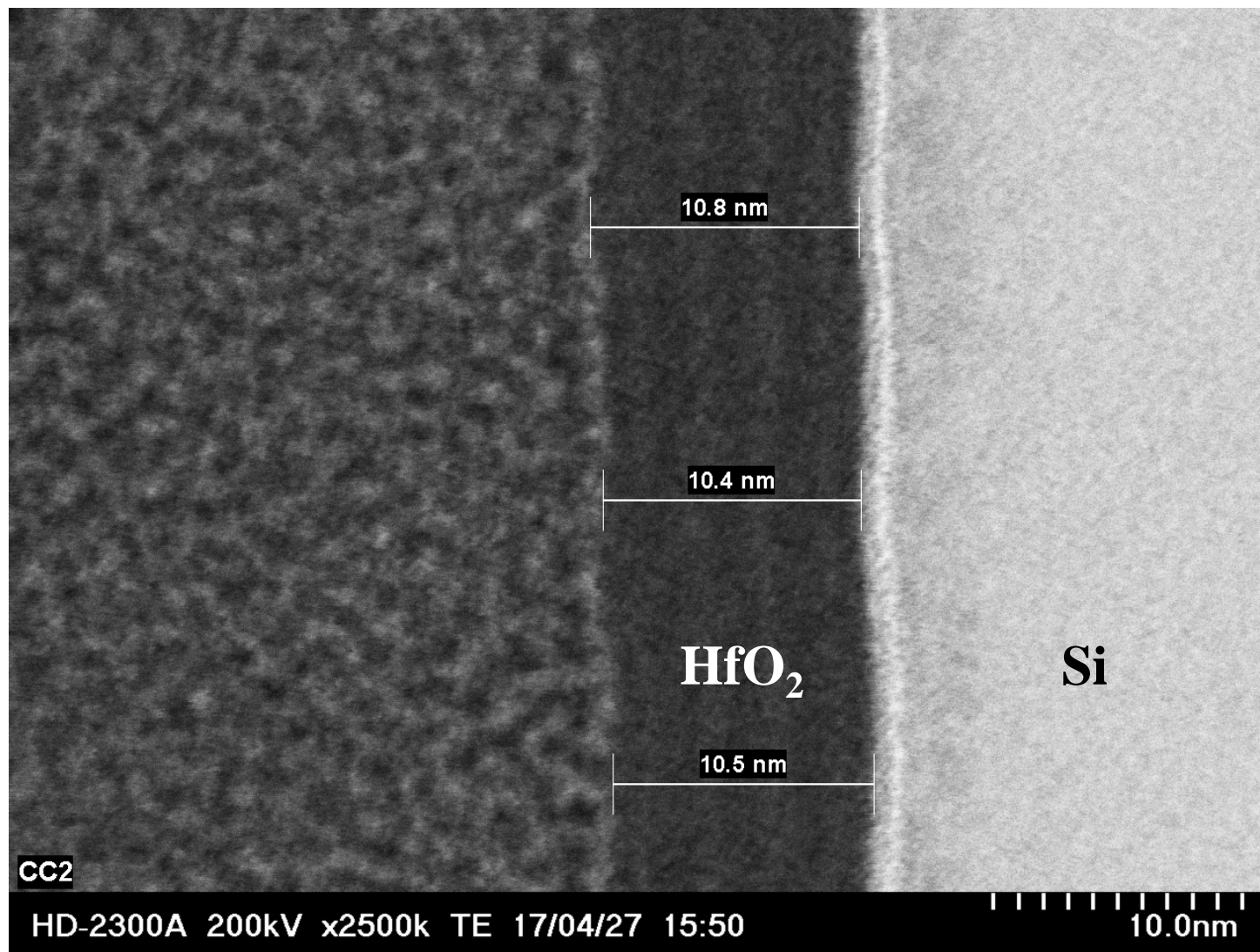


TEM and EELS

- TEM and EELS of HfO₂ samples demonstrated that the new Savannah ALD system at RIT is capable of depositing the desired material as well as the desired thickness (target 10nm)

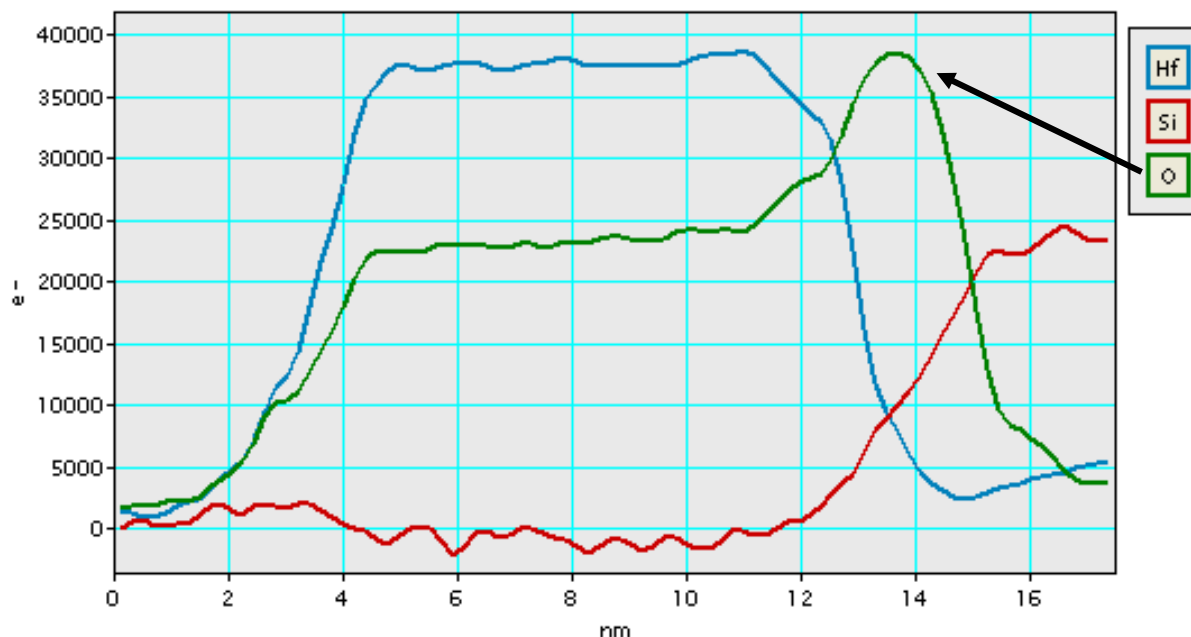


TEM and EELS



TEM and EELS

- Approximately 2 nm of interfacial oxide was present in Al doped samples
- Two capacitors in series



CV Measurements

- Modeling both capacitors in series with measured total capacitance:

$$C_{tot} \approx 190 \text{ pF}$$

$$t_{SiO_2} \approx 2 \times 10^{-7} \text{ cm}$$

$$\epsilon_0 = 8.854 \times 10^{-14} \text{ F/cm}$$

$$A \approx 2 \times 10^{-4} \text{ cm}^2$$

$$t_{Al:HfO_2} \approx 1 \times 10^{-6} \text{ cm}$$

$$C_{ox} = \frac{\epsilon A}{t} = \frac{\epsilon_0 \epsilon_r A}{t}$$

$$C_{tot} = \frac{1}{\frac{1}{C_{SiO_2}} + \frac{1}{C_{Al:HfO_2}}}$$

$$\therefore \epsilon_{rAl:HfO_2} \approx 23.9$$



Outline

- **Introduction**
 - Background
 - Previous Work at RIT
 - Project Objectives

- **Process**
 - ALD Development
 - Fabrication of Capacitor Devices

- **Results**
 - TEM and EELS
 - CV Measurements

- **Conclusions and Future Work**



Conclusions

- HfO₂ and Al:HfO₂ have both been successfully deposited by ALD at RIT
- Capacitors with the deposited materials were fabricated and tested
 - Low breakdown voltage as well as high leakage current was observed
- The dielectrics have been verified to be high k (≈ 25)



Future Work

- Fabrication variations will be considered to limit interfacial oxide
- A study of annealing conditions will be done to achieve the proper orthorhombic crystal phase
- XRD will be done to calculate crystal structure of film



Acknowledgements

- Dr. Santosh Kurinec, Jackson Anderson, and Karine Florent
- Patricia Meller and Dr. Karl Hirschman
- Dr. Ewbank and Dr. Pearson
- Spencer Pringle, Chris O'Connell, Joe McGlone, David MacMahon (Micron), NaMLaB, all of the amazing SMFL staff!



This work was supported in part by the National Science Foundation, Grant # ECCS-1541090

Any opinions, findings, and conclusions or recommendations expressed in this material are those of the author(s) and do not necessarily reflect the views of the National Science Foundation.



References

- [1] E. Yurchuk, J. Muller, J. Paul, T. Schlosser, D. Martin, R. Hoffmann, *et al.*, "Impact of Scaling on the Performance of HfO₂-Based Ferroelectric Field Effect Transistors," *Ieee Transactions on Electron Devices*, vol. 61, pp. 3699-3706, Nov 2014.
- [2] T. S. Boescke, J. Muller, D. Brauhaus, U. Schroder, and U. Bottger, "Ferroelectricity in hafnium oxide thin films," *Applied Physics Letters*, vol. 99, p. 3, Sep 2011.
- [3] T. Olsen, U. Schroder, S. Muller, A. Krause, D. Martin, A. Singh, *et al.*, "Co-sputtering yttrium into hafnium oxide thin films to produce ferroelectric properties," *Applied Physics Letters*, vol. 101, p. 4, Aug 2012.
- [4] U. Schroeder, S. Mueller, J. Mueller, E. Yurchuk, D. Martin, C. Adelmann, *et al.*, "Hafnium Oxide Based CMOS Compatible Ferroelectric Materials," *Ecs Journal of Solid State Science and Technology*, vol. 2, pp. N69-N72, 2013.
- [5] Ekateriana Yurchuk, "Electrical Characterisation of Ferroelectric Field Effect Transistors based on Ferroelectric HfO₂ Thin Films," Logos Verlag Berlin GmbH, Jun 30, 2015 - 238 pages
- [6] J. Muller, S. Muller, E. Yurchuk, "Ferroelectric Hafnium Oxide: A CMOS-compatible and highly scalable approach to future ferroelectric memories," Dresden, Germany
- [7] K. Florent, "Ferroelectric HfO₂ for Ferroelectric Field Effect Transistor (FeFET) and Ferroelectric Tunnel Junction (FTJ) Applications," RIT



THANK YOU!

